

AWI-324286-78

FS6B MASTER PCB CIRCUIT DIAGRAM

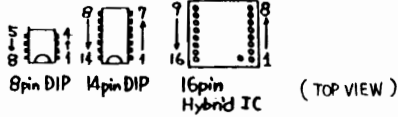
Functional block	Diagram No.	Contents
General information	A00	Index
	A01	Notes, Circuit blocks arrangement
	A02	Connector pin assignment
	A03	" " " "
MPU	AA1	Clock and Power-on-clear
	AA2	Microprocessor and Bus controller
	AA3	Bus ready control
	AA4	Interrupt control
	AA5	Power input and Engineering panel interface
	AA6	16.384MHz clock and Watch dog timer
	AA7	Timer and Baudrate generator
Bus Buffers	AB1	Address latch and buffer
	AB2	Main data bus tranceiver
	AB3	RAM data bus tranceiver and output control
Address decoder	AB4~5	Address decoder
ROM board	AC1	ROM board interface
RAM	AD1	RAM read/write control
	AD2	12K byte static RAM with parity bit
	AD3	Parity generator / checker
DMA	AE1	DMA control
	AE2	Address buffer for RAM
Bit op.	AE3	Bit operation
Serial I/O	AF1	Serial I/O interface
	AF2	Serial I/O control
	AF3	Transfer alarm logic and LED display
Bubble mem.	AG1~2	Bubble memory unit interface
PTR	AH1	Paper tape reader interface
Position display	AH2	Position display interface

Functional block	Diagram No.	Contents
MPG	AH3	Manual pulse generator interface
Expanded bus	AH4	Command pulse multiplexer for graphic display
	AH5~6	Expanded bus interface
Position control	AI1	Position control I/O
	AI2	Position control LSI
	AI3	X-axis position feedback
	AI4	Y-axis position feedback
	AI5	Z-axis position feedback
	AI6	Reference voltage and D/A converter
	AI7	F/V converter (velocity feedback)
	AI8	Interface for DSCG adapter
	AI9	GT Analog spindle output, Velocity control unit interface
NC/TC	AJ1	Feedback pulse output
	AJ2	General purpose interface

INDEX

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注方: 1 本回路図ではICピン次の例のように数える
 NOTES: In this circuit diagram, IC pins are numbered as following examples.

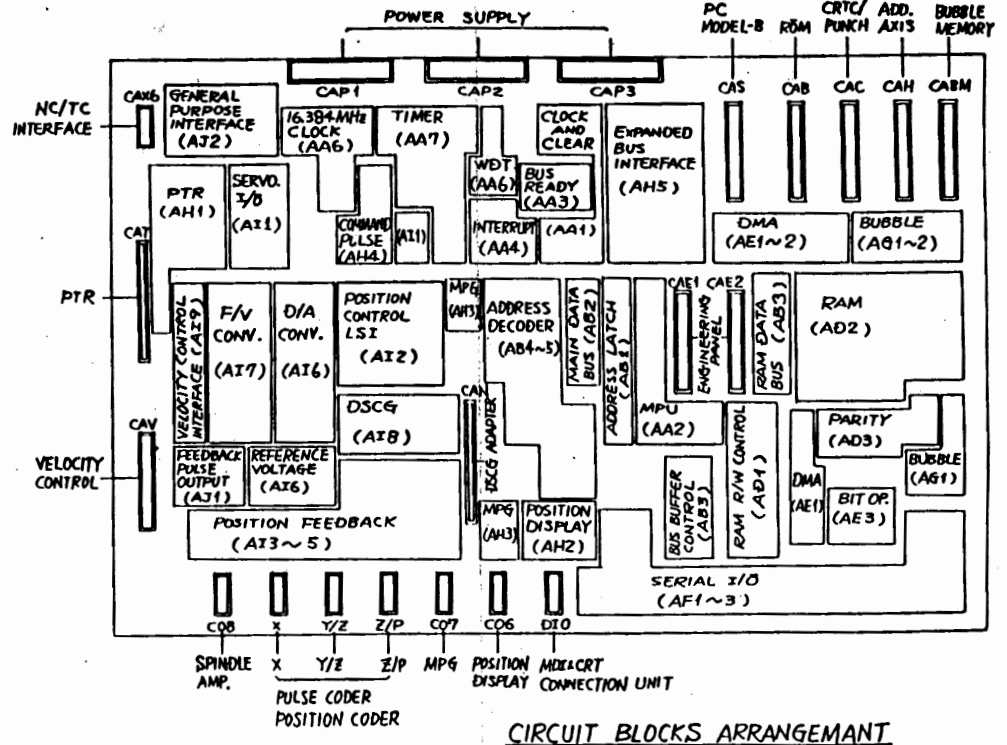


2 本プリント基板は3軸分の位置制御回路と持て1136[△] 其の役割は標準6Tと6Mとは次の表のようになる。
 Position control circuit of this PCB consists of three subsystems, and they are assigned as following table in case of standard 6T or standard 6M.

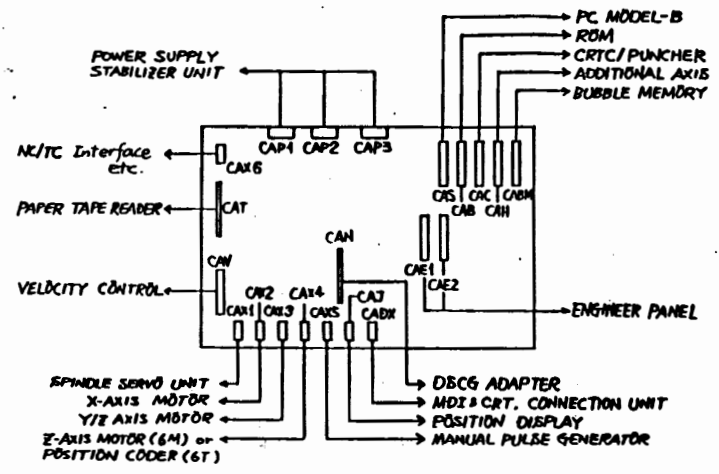
	1st axis X1軸	2nd axis Y2軸	3rd axis Z3軸
6T	X	Z	Spindle control
6M	X	Y	Z

本回路図中では X1軸~X3軸に示して X,Y,Z となり標準6Mの軸名は信号名称の添字に使用している
 従って 6Tの場合 信号名称を A1~A3に示すように 呼びかえり必要がある。
 In this circuit diagram, letters X,Y and Z are used for the suffix of signal name to distinguish each axis. Thus signal names are for standard 6M, and they should be renamed as shown in page 4.

3 △印は誤記訂正あるいは情報の追加を意味するものであり 特に注のない限り本プリント基板版数について適用するものとする。
 The mark △ means errata correction or addition of some informations, and is applied to any PCB edition otherwise noted.



WI-3242BB



POWER INPUT

CAP1

6	5	4	3	2	1
OV	OV	OV	+5V	+5V	+5V

BLACK

CAP2

6	5	4	3	2	1
+5V	OV	EN	PPF	OV	+5V

WHITE

CAP3

6	5	4	3	2	1
+24V	+24V	OV	OV	+5V	+5V

BROWN

(see AAS)

PTR

CAT

	A	B
1	P1	OV
2	P2	OV
3	P3	OV
4	P4	OV
5	PS	OV
6	P6	OV
7	P7	OV
8	P8	OV
9	SP	OV
10	RTER	OV
11	ROT	OV
12	HRMT	OV
13	FDT	OV
14	OPT	OV
15	HRWT	OV
16	RCL	OV
17	RRVT	OV
18	ALM	OV
19	+15V	OV
20	RREL	OV
21	+5V	OV
22	+5V	OV
23	+24V	OV
24	OH1X	OH1Y
25	RMT	

(see AH1)

DSCG Adapter

CAN

	A	B
1	RIPX	HDSCG
2	RISX	+5V
3	RIPY	+5V
4	RISY	+5V
5	RIPZ	+5V
6	RISZ	+5V
7	RDESHX	+5V
8	RDESHY	OV
9	RDESHZ	
10	RSIN1X	RSIN2X
11	OV	RCOS1X
12	RCOS2X	OV
13	RSIN1Y	RSIN2Y
14	OV	RCOS1Y
15	RCOS2Y	OV
16	RSIN1Z	RSIN2Z
17	OV	RCOS1Z
18	RCOS2Z	OV
19	+15V	OV
20	-15V	OV
21	TSAX	TSBY
22	TSAY	TSBZ
23	TSAZ	TSBZ
24	OH1X	OH1Y
25	OH1Z	

(see AIB)

Engineering panel

CAE1, CAE2

50	+5V	18	+5V
49	RCLKO	17	+5V
48	OV	52	RSO
47	RNMTEP	31	RSO
46	RREADY	30	RSY
45	RDEYEP	29	RSZ
44	OV	28	OV
43	RPCLO	27	RBOGT0
42	RRESET	26	OV
41	OV	25	REFI1
40	ALE	24	RHEP
39	DEN	23	AD19
38	DIR	22	AD18
37	OV	21	AD17
36	RMRDC	20	AD16
35	RNMTC	19	AD15
34	REFI2		
33	RENS		
		16	OE1
		15	AD00
		14	AD01
		13	AD02
		12	AD03
		11	AD04
		10	AD05
		9	AD06
		8	AD07
		7	AD08
		6	AD09
		5	AD10
		4	AD11
		3	AD12
		2	AD13
		1	AD14

(see AAS)

PC model B

CAS

50	D15X	16	D14X
49	D15X	17	D12X
48	D11X	16	D09X
47	D08X	31	D07X
46	D08X	30	D04X
45	D02X	30	D04X
44	EIRDY	29	DOOR
43	+15V	28	REIRRG
42	OV	27	RPLC1
41	-15V	26	RDTR
40	RCLKO	25	RMRDCX
39	RBAUD	24	RNMWCK
38	RA16X	23	REIDEN
37	RA15X	22	RA15X
36	RA10X	21	RA12X
35	RA07X	20	RA07X
34	RA04X	19	RA06X
33	RA02X		
		18	DA4
		17	D12X
		15	D06X
		14	D03X
		13	D01X
		12	+5V
		11	+5V
		10	+5V
		9	OV
		8	OV
		7	OV
		6	RA14X
		5	RA11X
		4	RA08X
		3	RA03X
		2	RA03X
		1	RA01X

(see AH6)

ROM

CAB

50	RDB15	16	RDB14
49	RDB15	17	RDB12
48	RDB11	16	RDB09
47	RDB08	31	RDB07
46	RDB05	30	RDB04
45	RDB02	30	RDB04
44	VPP	29	RDB00
43		28	RFA1M
42	OV	27	RADROM
41	RDMAN	26	RDTR
40	RSS	25	VPPOUT
39	BS	24	STB
38	RA16X	23	RA17X
37	RA15X	22	RA15X
36	RA10X	21	RA12X
35	RA07X	20	RA07X
34	RA04X	19	RA06X
33	RA02X		
		18	RDB14
		17	RDB12
		15	RDB06
		14	RDB03
		13	RDB01
		12	+5V
		11	+5V
		10	+5V
		9	OV
		8	OV
		7	OV
		6	RA14X
		5	RA11X
		4	RA08X
		3	RA03X
		2	RA03X
		1	RA01X

(see AC4)

CRTC/PUNCHER

CAC

50	R+MP	16	R-MP
49	R-MP	17	R-LP
48	D11X	32	D10X
47	D08X	31	D07X
46	D05X	30	D04X
45	D02X	30	D04X
44	EIRDY	29	DOOR
43	+24V	28	REIRRG
42	OV	27	RPLC1
41	-15V	26	RDTR
40	RCLKO	25	RMRDCX
39	RBAUD	24	RNMWCK
38	RA16X	23	REIDEN
37	RA15X	22	RA15X
36	RA10X	21	RA12X
35	RA07X	20	RA07X
34	RA04X	19	RA06X
33	RA02X		
		18	R-MP
		17	R-LP
		15	D06X
		14	D03X
		13	D01X
		12	+5V
		11	+5V
		10	+5V
		9	OV
		8	OV
		7	OV
		6	RA14X
		5	RA11X
		4	RA08X
		3	RA03X
		2	RA03X
		1	RA01X

(see AH6)

Additional Axis

CAH

50	R+MP	16	R-MP
49	R+LP	17	R-LP
48	RITPX	32	RCLPROY
47	D08X	31	D07X
46	D05X	30	D04X
45	D02X	30	D04X
44	EIRDY	29	DOOR
43	+15V	28	REIRRG
42	OV	27	RPLC1
41	-15V	26	RDTR
40	RCLKO	25	RMRDCX
39	RC16MX	24	RNMWCK
38	RA16X	23	REIDEN
37	RA15X	22	RA15X
36	RA10X	21	RA12X
35	RA07X	20	RA07X
34	RA04X	19	RA06X
33	RA02X		
		18	R-MP
		17	R-LP
		15	D06X
		14	D03X
		13	D01X
		12	+5V
		11	+5V
		10	+5V
		9	OV
		8	OV
		7	OV
		6	RA14X
		5	RA11X
		4	RA08X
		3	RA03X
		2	RA03X
		1	RA01X

(see AH6)

Bubble memory

CABM

50	+24V	1	+24V
49		2	
48		3	-15V
47		4	
46	OV	21	+5V
45		22	+5V
44	RC2BM	23	+5V
43	RS2BM	24	+5V
42	RS1BM	25	OV
41	RS0BM	26	OV
40		27	OV
39		28	OV
38		29	OV
37		30	OV
36		31	OV
35		32	OV
		18	+24V
		17	-15V
		16	+5V
		15	+5V
		14	+5V
		13	+5V
		12	+5V
		11	+5V
		10	+5V
		9	+5V
		8	+5V
		7	+5V
		6	+5V
		5	+5V
		4	+5V
		3	+5V
		2	+5V
		1	+5V

(see AG2)

NC/TC Interface etc

CAX6

14	DA7	1	DA0
15	RCTL0	2	DA1
16	RCTL1	3	DA2
17	RCTL2	4	DA3
18		5	DA4
19	+5V	6	DA5
20	5G	7	DA6

(see AJ2)

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6T

6T

Velocity control unit

CAV

33	TSAX	1	VCMDX
34	TSAX	2	VCMDZ
35	TSAX	3	VCMDZ
36	ECZ	4	ECX
37	SG	5	SG
38	SG	6	SG
39	SG	7	VCMDZ
40	SG	8	NFBZ
41	NFBX	9	NFBX
42	VRDY	10	VRDYX
43	EIZ	11	EIX
44	OVLZ	12	OVLX
45	EZZ	13	EZX
46	NFBX	14	PFBZ
47	PFBX	15	NFBZ
48	CRZ	16	PROYX
49		17	ENBLX
50		18	CRX

(see AI9)

Analog spindle output

CAX1 (CO8)

14		1	
15	CRB	2	ENBLE
16		3	
17		4	
18		5	
19	ECB	6	
20	SG	7	VCMDZ

(see AI9)

X axis feedback

CAX2 (X)

14	PCZX	1	OV
15	#PCZX	2	OV
16	PCAX	3	OV
17	#PCAX	4	+5V
18	PCBX	5	+5V
19	#PCBX	6	+5V
20	SG	7	

(see AI3)

Z axis feedback

CAX3 (YIZ)

14	PCZZ	1	OV
15	#PCZZ	2	OV
16	PCAZ	3	OV
17	#PCAZ	4	+5V
18	PCBZ	5	+5V
19	#PCBZ	6	+5V
20	SG	7	

(see AI4)

Position coder

CAX4 (ZIP)

14	SC	1	OV
15	#SC	2	OV
16	PA	3	OV
17	#PA	4	+5V
18	PB	5	+5V
19	#PB	6	+5V
20	SG	7	

(see AI5)

MPG and Magneswitch

CAX5 (CO7)

14	SC	1	OV
15	NHC	2	OV
16	PA	3	OV
17	#PA	4	+5V
18	PB	5	+5V
19	#PB	6	+5V
20	SG	7	

(see AH3)

Position display

CAJ (CO6)

14	OV	1	OV
15	+5V	2	OV
16	+5V	3	NRSX
17	+5V	4	NRSZ
18	+5V	5	
19	+5V	6	
20	SG	7	OV

(see AH2)

MDI & CRT and connection unit

CADX (DIO)

1	SD0	14	MSRDY
2	#SD0	15	#MSRDY
3	STB	16	SLRDY
4	#STB	17	#SLRDY
5	STA	18	SDI
6	#STA	19	#SDI
7	OV	20	SG

(see AF1)

注1. 信号名称は6T用に書きかえてあります
 2. 6Tで使用しない信号は省略してあります。実際には6M用の信号が来ていることがあるので注意が必要です。6M用のピン配列と対照して下さい。

NOTES:
 1. Signal names are renamed for 6T.
 2. This diagram omits the signals which are not used in 6T. But notice that some signals for 6M may be available at pins without signal name. Compare with the diagram for 6M shown below.

6M

6M

Velocity control unit

CAV

33	TSAX	1	VCMDX
34	TSAY	2	VCMDY
35	TSAZ	3	VCMDZ
36	ECZ	4	ECX
37	SG	5	SG
38	SG	6	SG
39	SG	7	PFBZ
40	NFBZ	8	NFBY
41	NFBX	9	NFBY
42	VRDY	10	VRDYX
43	EIZ	11	EIX
44	OVLZ	12	OVLX
45	EZZ	13	EZX
46	NFBX	14	PFBY
47	PFBY	15	NFBY
48	PROYZ	16	PROYX
49	ENBLZ	17	ENBLX
50	CRZ	18	CRX

(see AI9)

not used

CAX1 (CO8)

14		1	
15	CRZ	2	ENBLE
16		3	
17		4	
18		5	
19	ECZ	6	
20	SG	7	VCMDZ

(see AI9)

X axis feedback

CAX2 (X)

14	PCZX	1	OV
15	#PCZX	2	OV
16	PCAX	3	OV
17	#PCAX	4	+5V
18	PCBX	5	+5V
19	#PCBX	6	+5V
20	SG	7	

(see AI3)

Y/Z axis feedback

CAX3 (YIZ)

14	PCZY	1	OV
15	#PCZY	2	OV
16	PCAY	3	OV
17	#PCAY	4	+5V
18	PCBY	5	+5V
19	#PCBY	6	+5V
20	SG	7	

(see AI4)

Z/P axis feedback

CAX4 (ZIP)

14	PCZZ	1	OV
15	#PCZZ	2	OV
16	PCAZ	3	OV
17	#PCAZ	4	+5V
18	PCBZ	5	+5V
19	#PCBZ	6	+5V
20	SG	7	

(see AI5)

MPG and Magneswitch

CAX5 (CO7)

14	PCZZ	1	OV
15	#PCZZ	2	OV
16	PCAZ	3	OV
17	#PCAZ	4	+5V
18	PCBZ	5	+5V
19	#PCBZ	6	+5V
20	SG	7	

(see AH3)

Position display

CAJ (CO6)

14	OV	1	OV
15	+5V	2	OV
16	+5V	3	NRSX
17	+5V	4	NRSY
18	+5V	5	NRSZ
19	+5V	6	NRS4
20	SG	7	OV

(see AH2)

MDI & CRT and connection unit

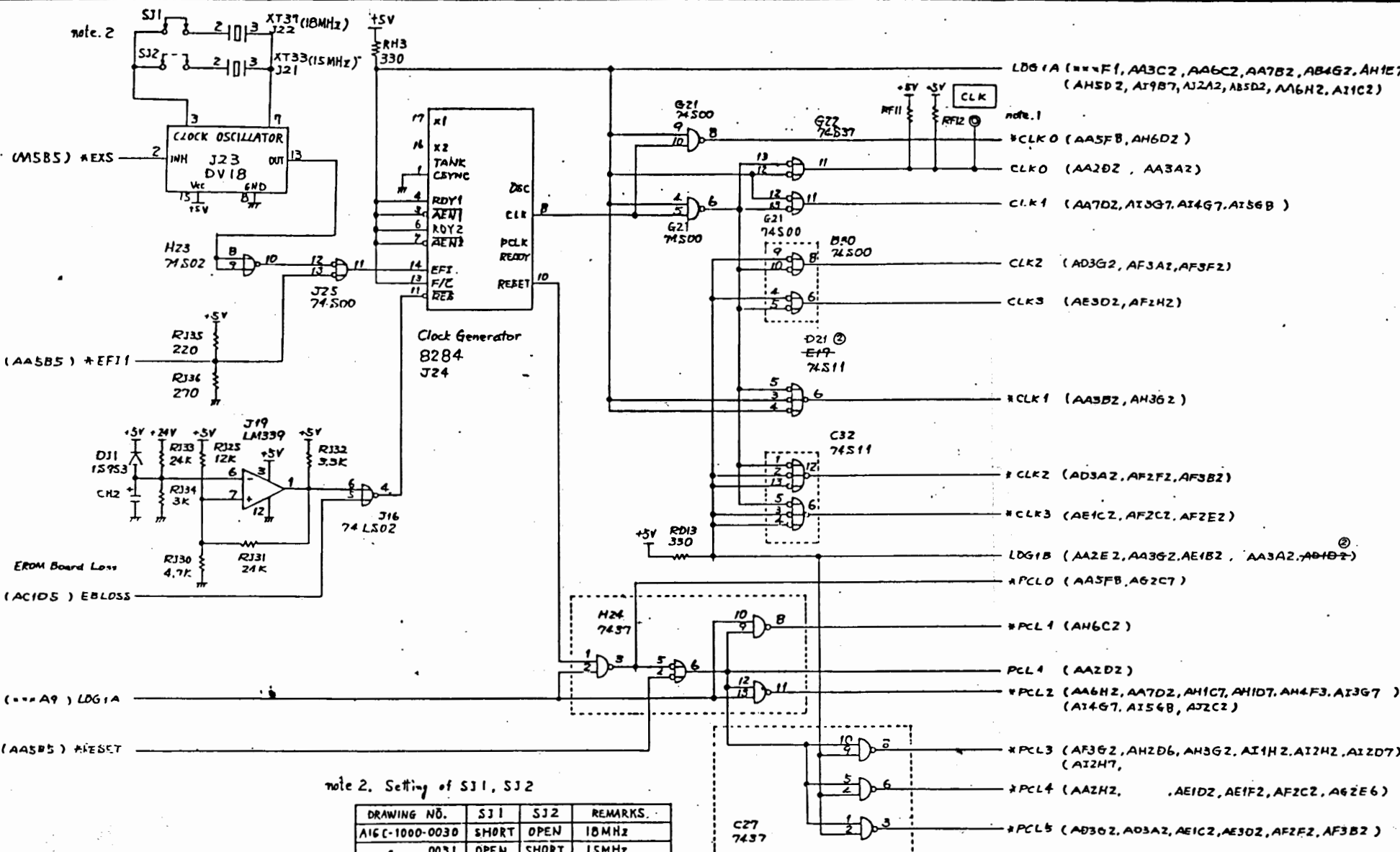
CADX (DIO)

1	SD0	14	MSRDY
2	#SD0	15	#MSRDY
3	STB	16	SLRDY
4	#STB	17	#SLRDY
5	STA	18	SDI
6	#STA	19	#SDI
7	OV	20	SG

(see AF1)

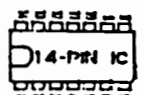
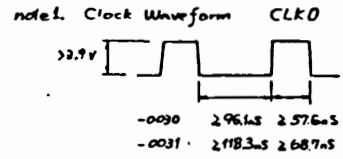
CONNECTOR PIN ASSIGNMENT

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note 2. Setting of SJ1, SJ2

DRAWING NO.	SJ1	SJ2	REMARKS
A16C-1000-0030	SHORT	OPEN	10MHz
0031	OPEN	SHORT	15MHz



CLOCK AND POWER-ON-CLEAR

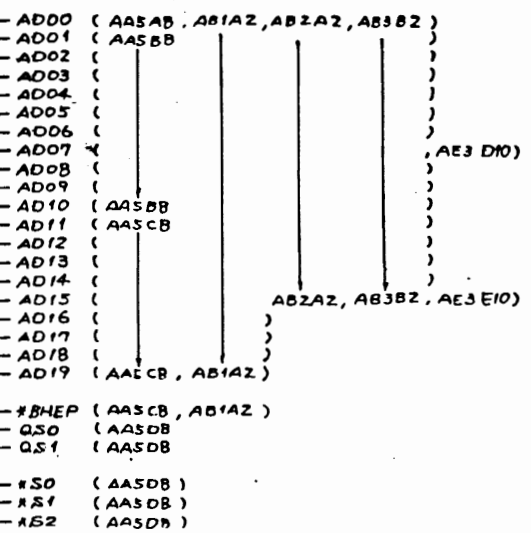
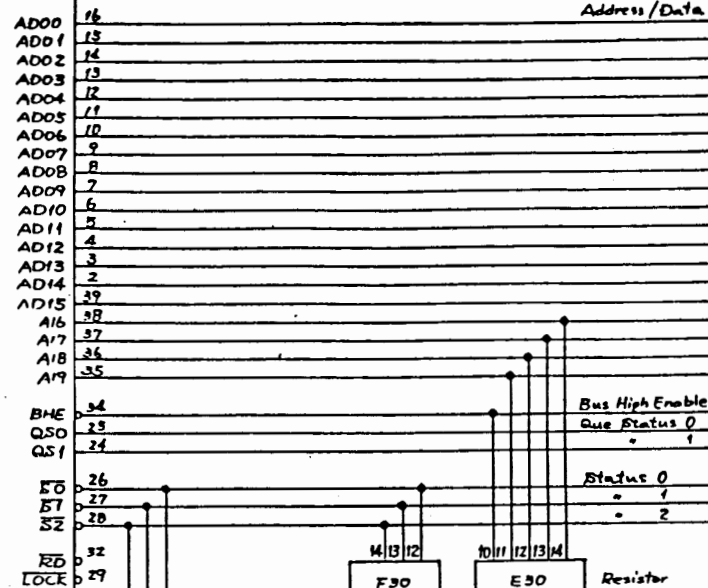
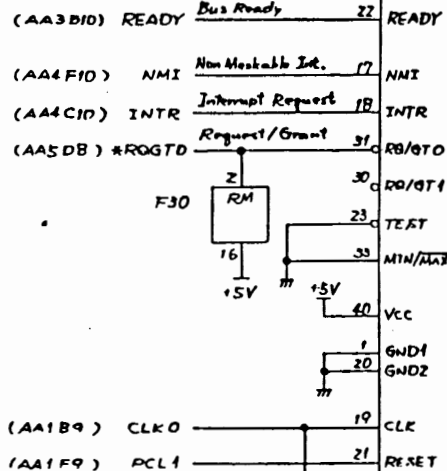
F56 model B MASTER PCB

A A I

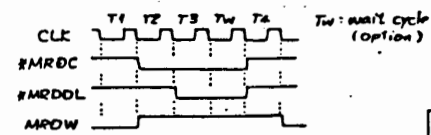
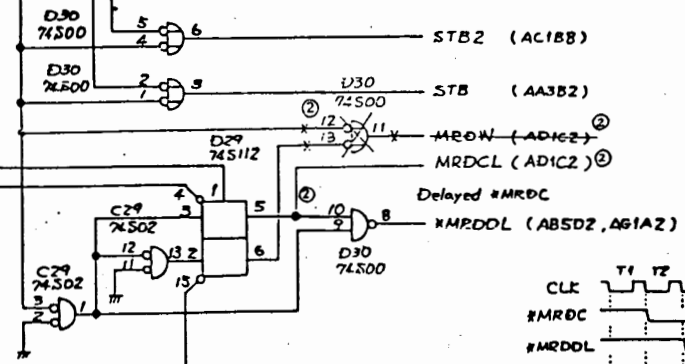
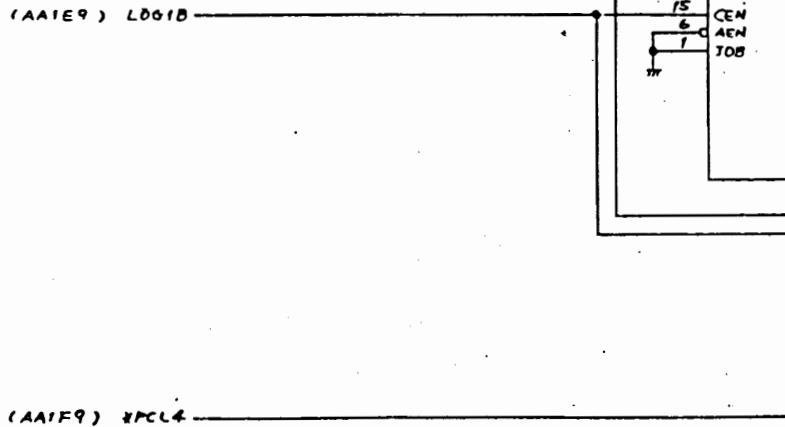
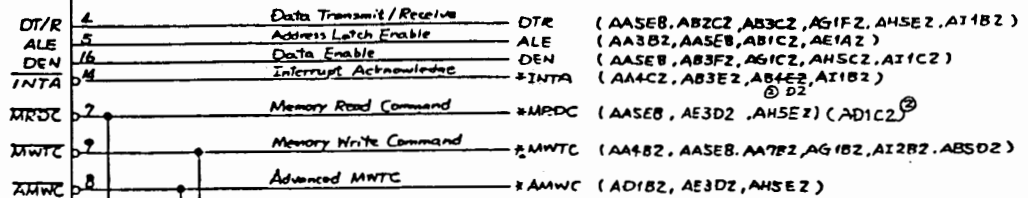
A16C-1000-0030		0031	
0030		0031	
0030		0031	

AWI-3242B1

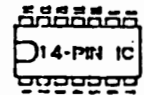
Microprocessor
8086-2
E26



Bus Controller
8288
D27



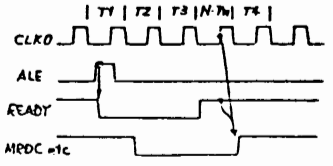
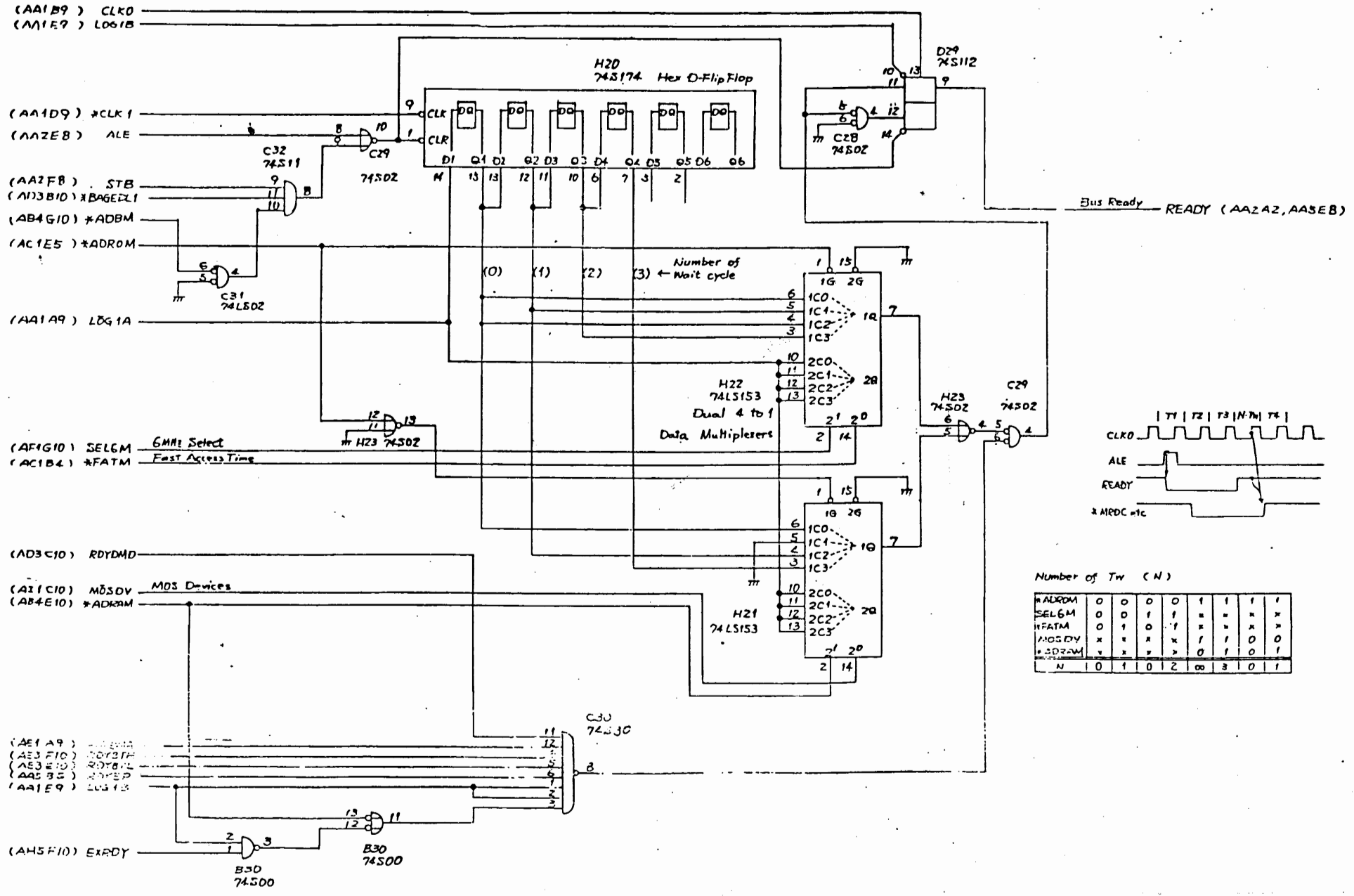
(AA1F9) *PCL4



MICROPROCESSOR AND BUS CONTROLLER

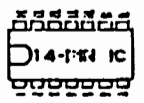
AA2	
A16C-1000-0030	
0031	
101	

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Number of Tw (N)

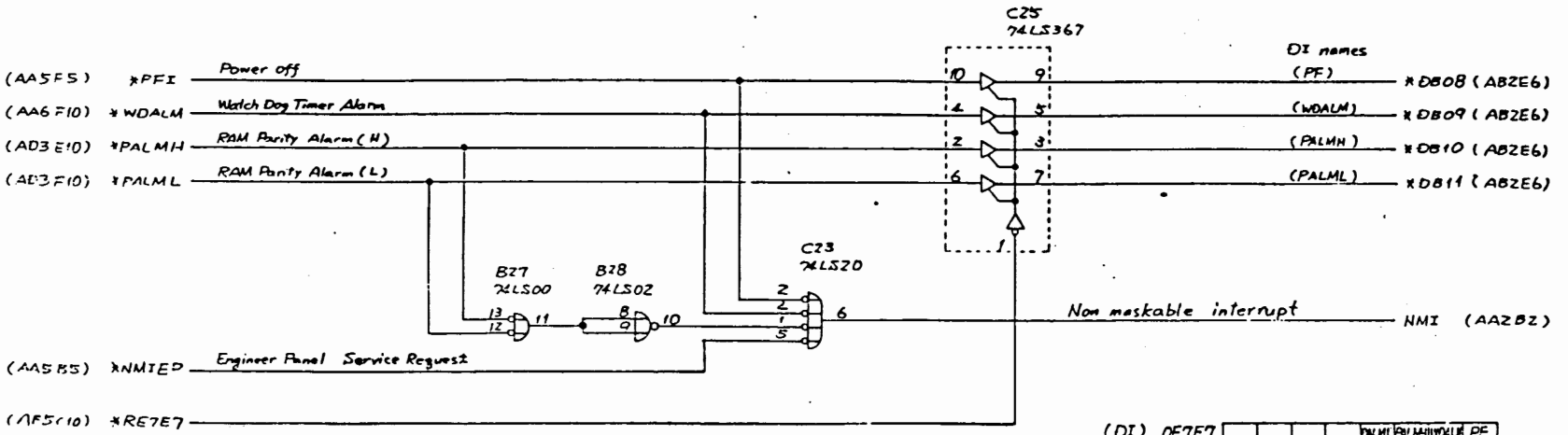
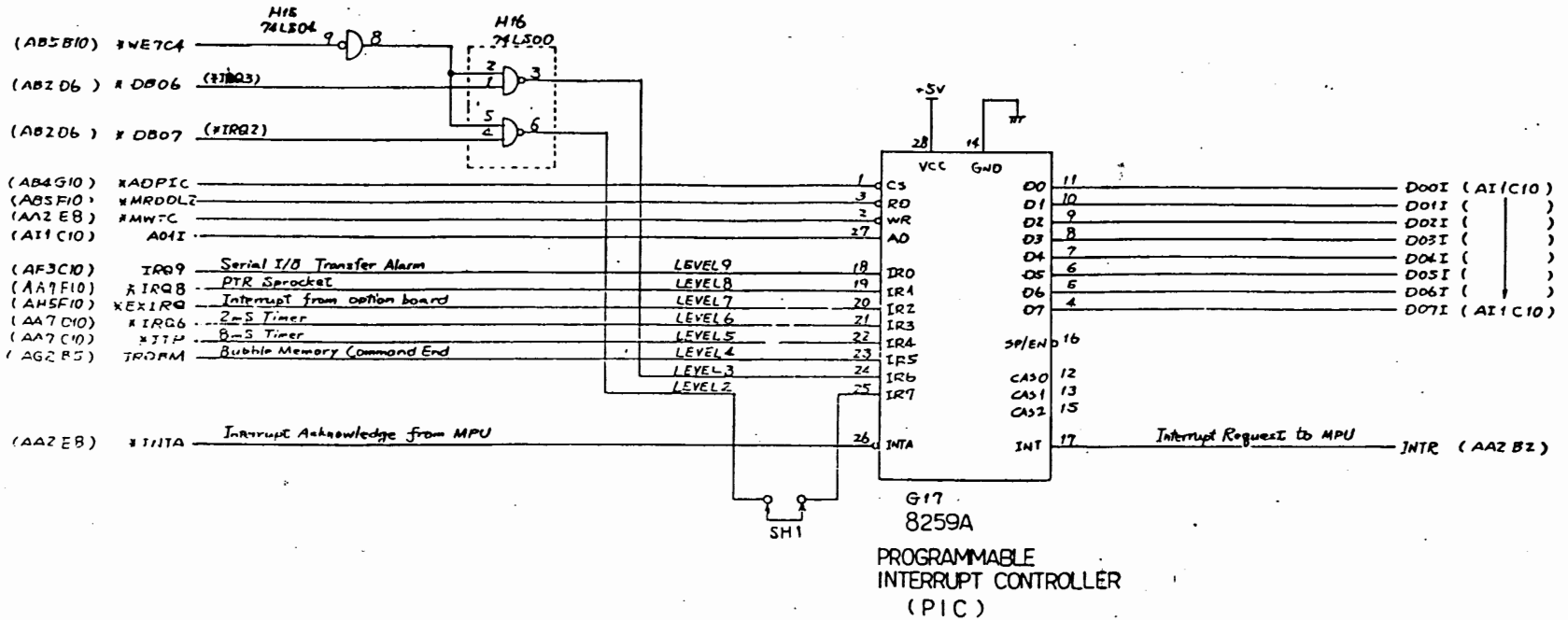
*ADRM	0	0	0	1	1	1	1
SELEM	0	0	1	1	1	1	1
*FATM	0	1	0	1	1	1	1
MDSOV	x	1	x	x	1	1	0
*ADRM	x	x	x	x	0	1	0
N	0	1	0	2	3	0	1



BUS READY CONTROL

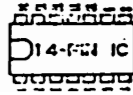
AA3

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(DI) 0E7E7 PALMLPALMHWDALM PF

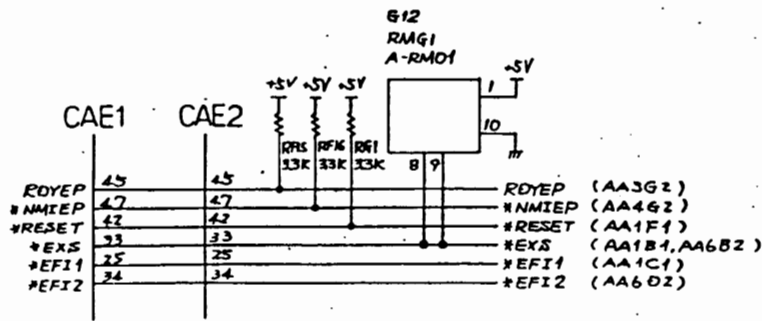
(DB) 0E7C4 #IR03#IR02 ITPE



INTERRUPT CONTROL LOGIC

AWI-324294

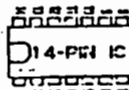
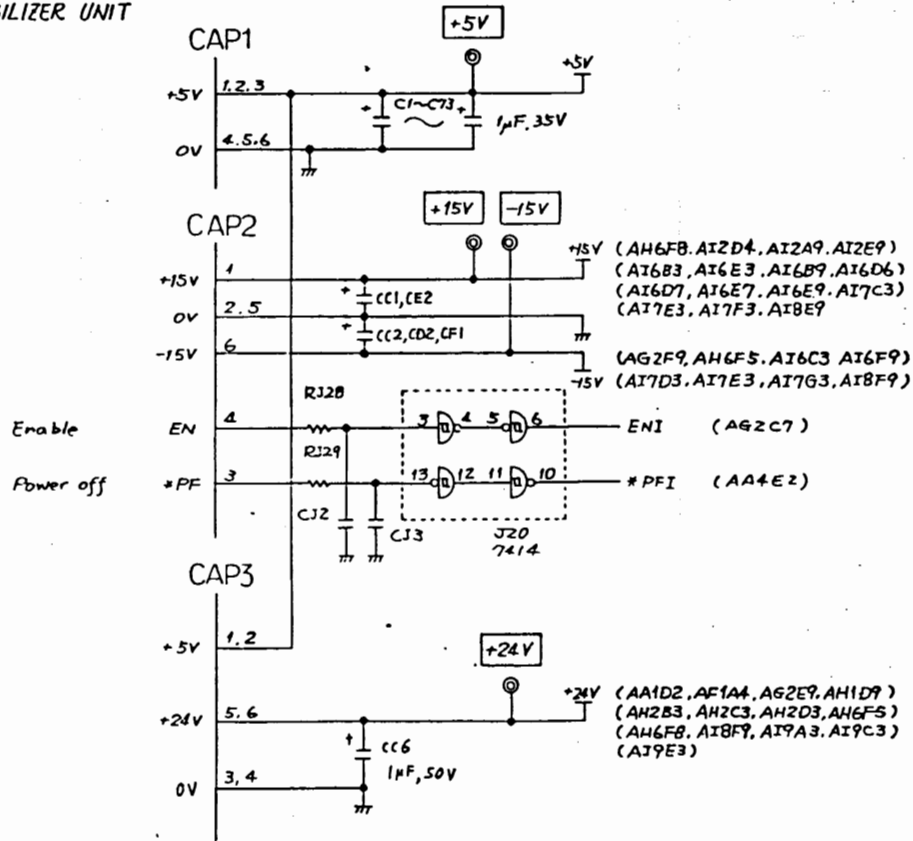
(Not used)



CAE1 CAE2 (Not used)

(AA2AB)	AD00	15	15	AD00
()	AD01	14	14	AD01
()	AD02	13	13	AD02
()	AD03	12	12	AD03
()	AD04	11	11	AD04
(AA2AB)	AD05	10	10	AD05
(AA2BB)	AD06	9	9	AD06
()	AD07	8	8	AD07
()	AD08	7	7	AD08
()	AD09	6	6	AD09
()	AD10	5	5	AD10
()	AD11	4	4	AD11
()	AD12	3	3	AD12
()	AD13	2	2	AD13
()	AD14	1	1	AD14
()	AD15	19	19	AD15
(AA2BB)	AD16	20	20	AD16
(AA2CB)	AD17	21	21	AD17
()	AD18	22	22	AD18
()	AD19	23	23	AD19
(AA2CB)	*BHEP	24	24	*BHEP
(AA2CB)	*S0	31	31	*S0
(AA2DB)	*S1	30	30	*S1
(AA2DB)	*S2	29	29	*S2
(AA2B2)	*RQGT0	27	27	*RQGT0
(AA2CB)	Q50	32	32	Q50
(AA2CB)	Q51	16	16	Q51
(AA2EB)	*MRDC	36	36	*MRDC
(AA2EB)	*MWTC	35	35	*MWTC
(AA2EB)	ALE	40	40	ALE
(AA2EB)	DEN	39	39	DEN
(AA2EB)	DTR	38	38	DTR
(AA3B10)	READY	46	46	*READY
(AA1E9)	*PCLO	43	43	*PCLO
(AA1B9)	*CLKO	49	49	*CLKO
	+5V	17,18,50	17,18,50	+5V
	OV	26,28,37,41,44,48	26,28,37,41,44,48	OV

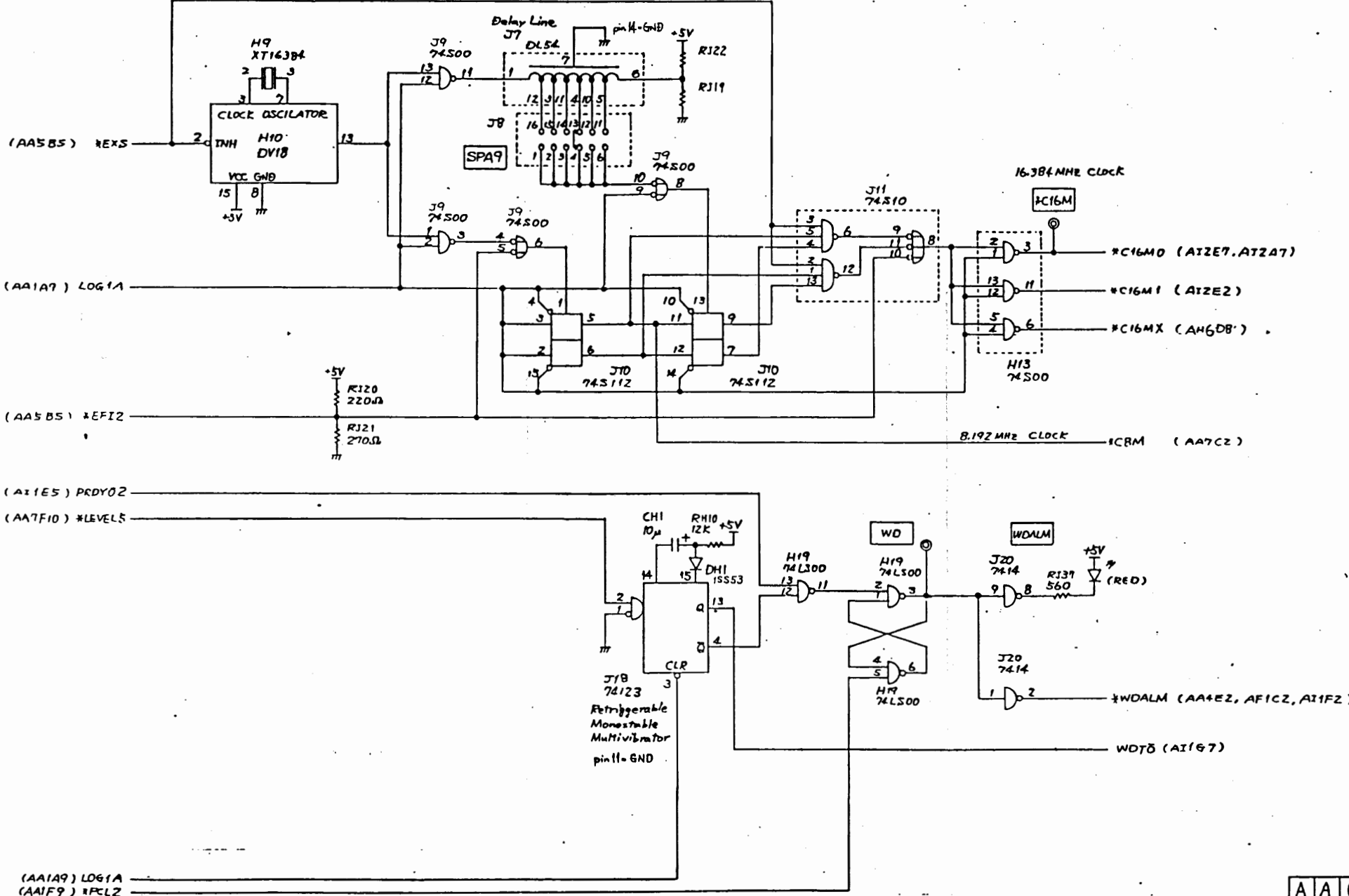
FROM STABILIZER UNIT



POWER INPUT

AA5

AWL-324295



14-PIN IC

16.384MHz CLOCK AND WATCH DOG TIMER

AA6

A16C-1000-0030/01

AWI-324296

(A11C10) D00T~D07T

(A11C10) A0ZT
(A11C10) A01T

(A84610) *ADPIT
(A85F10) *MRDDLZ
(AA2E8) *MWTC

(***H10) C614K
(AA1A9) LOG1A

(AA6D10) *CBM

(AA1F9) *PCLZ

(AA1B9) CLK1

(A85B10) *WE7C4
(A82C6) *DB00

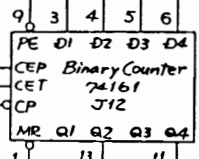
(***D10) *TMR1

(A41G5) *TSP

(A41F10) FDT0



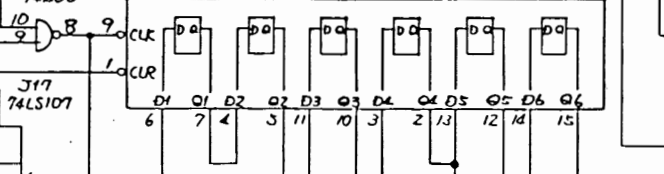
Programmable Interval Timer
G15 8253-5



512KHz

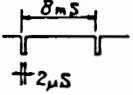


J13 74LS174



Interrupt of PTR sprocket

*ITTP



*ITPK (A46F8)

Interpolation Start

*ITP (A12B1, AA4C2)

*TMR1 (***E2)

2ms timer

*IRQ6 (AA4C2)

Baudrate

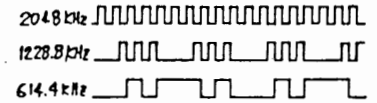
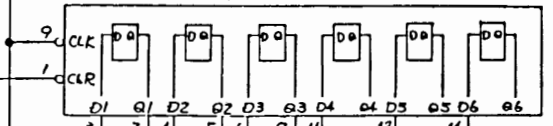
*BAUD (A46E2)

*IRQ8 (AA4B2)

*LEVELS (AA6E2)

2048 kHz

J13 74LS174

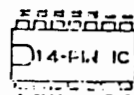


1228.8 kHz

J17 74LS107

614.4 kHz

C614K (***B2)



TIMER AND BAUDRATE GENERATOR

AA7

AA16C-1000-0030
0031/01

AWI-324297

(AAZAB)
(AAZBB)
(AAZCB) AD00~AD19

(AAZCB) #BHEP

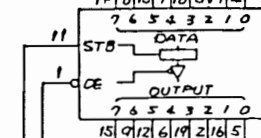
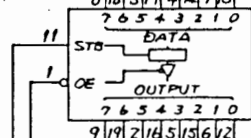
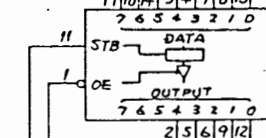
(AAZE8) ALE

D25
74LS373

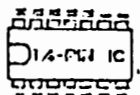
E23
74LS373

F23
74LS373

Octal Latch



- AB00 (AB5B2, AE2D2)
- AB01 (AB5C2, AE2D2, AHS2, AT1C2)
- AB02 (AB5C2, AE2D2, AHS2, AT1C2)
- AB03 (AB5B2, AE2D2, AHS2, AT1C2)
- AB04 (AB4G2, AB5B2, AE2D2, AHS2, AT1C2)
- AB05 (AB4H2, AB5B2, AE2D2, AHS2, AT1C2)
- AB06 (AB4G2, AE2D2, AHS2)
- AB07 (AB4G2, AE2D2, AHS2)
- AB08 (AB4G2, AE2D2, AHS2)
- AB09 (AB4F2, AE2D2, AHS2)
- AB10 (AB4F2, AE2D2, AHS2)
- AB11 (AB4E2, AE2D2, AHS2)
- AB12 (AB4E2, AE2D2, AE3B2, AHS2)
- AB13 (AB4D2, AE3B2, AHS2) (AE2D2)
- AB14 (AB4D2, AE3B2, AHS2) (AE2D2)
- AB15 (AB4D2, AE3D2, AHS2)
- AB16 (AB4B2, AHS2)
- AB17 (AB4B2, AHS2)
- AB18 (AB4B2)
- AB19 (AB4A2)
- #BHE (AE2D2)



ADDRESS LATCH AND BUFFER

A	B	I

AWI-324297

1000
36

AWI-32429B

(AA1A8) AD00~AD15
(AA2B8)

E25
74LS640

F25
74LS640

DIGITAL DATA
TRANCEIVERS

F30

F30

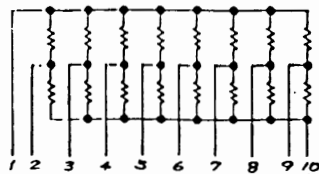
Resistor module
ISA07

(AA2E8) DTR

(AB3E9) XENTS

- *DB00 (AA7E2, AC1D8, AH1B5, AH1B7, AH3E10, AH4E3, AH5C2, A11A2, A11D2, A11D10, A18E4, AJ2C2)
- *DB01 (AC1D8, AH1B5, AH1B7, AH3E10, AH4E3, AH5C2, A11A2, A11D2, A11D10, A18F4, AJ2C2)
- *DB02 (AC1E8, AH1B5, AH1F5, AH1C7, AH3E10, AH4E3, AH5C2, A11A2, A11E2, A11D10, A18F4, AJ2C2)
- *DB03 (AC1E8, AH1B5, AH1D5, AH1C7, AH3E10, AH4E3, AH5C2, A11A2, A11D10, A19F5, AJ2C2)
- *DB04 (AC1E8, AH1C5, AH1E5, AH1D7, AH3E10, AH5C2, A11A2, A11E2, A11D10, A13G7, A19B5, AJ2D2)
- *DB05 (AC1E8, AH1C5, AH1E5, AH1D7, AH3E10, AH5C2, A11A2, A11D10, A19C5, AJ2D2)
- *DB06 (AA4A2, AC1E8, AH1C5, AH1D5, AH1D7, AH3D10, AH5C2, A11A2, A11E2, A11D10, A18F4, AJ2D2)
- *DB07 (AA4A2, AC1E8, AH1C5, AH1E5, AH1D7, AH3E10, AH5C2, A11A2, A11E2, A11D10, AJ2D2) (A19E5)
- *DB08 (AA4E10, AC1E8, AE1E2, AH2B5, AH2C6, AH5C2, A11B2, AF1G10, AF3F2)
- *DB09 (AA4E10, AC1E8, AE1E2, AF1F10, AH2C5, AH2C6, AH5C2, A11G2, AF3F2)
- *DB10 (AA4E10, AC1E8, AE1E2, AF1F10, AH2C5, AH2C6, AH5C2, A11G2, AF3F2)
- *DB11 (AA4F10, AC1E8, AE1E2, AF1F10, AH2D5, AH2C6, AH5C2, AF3F2)
- *DB12 (AC1F8, AE1E2, AF1F10, AG2D5, AH2C6, AH5C2, AF3G2)
- *DB13 (AC1F8, AE1E2, AG2C5, AH5C2, AF3G2)
- *DB14 (AC1F8, AF1F10, AG2C5, AG2E6, AH5C2, AD3F2, AF3E2)
- *DB15 (AC1F8, AG2C5, AG2D6, AH5C2)

Resistor module A-RM01



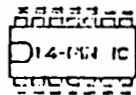
10pin STP

+5V

+5V

RMJ1
A-RM01

RMJ2
A-RM01



MAIN DATA BUS TRANCEIVER

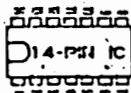
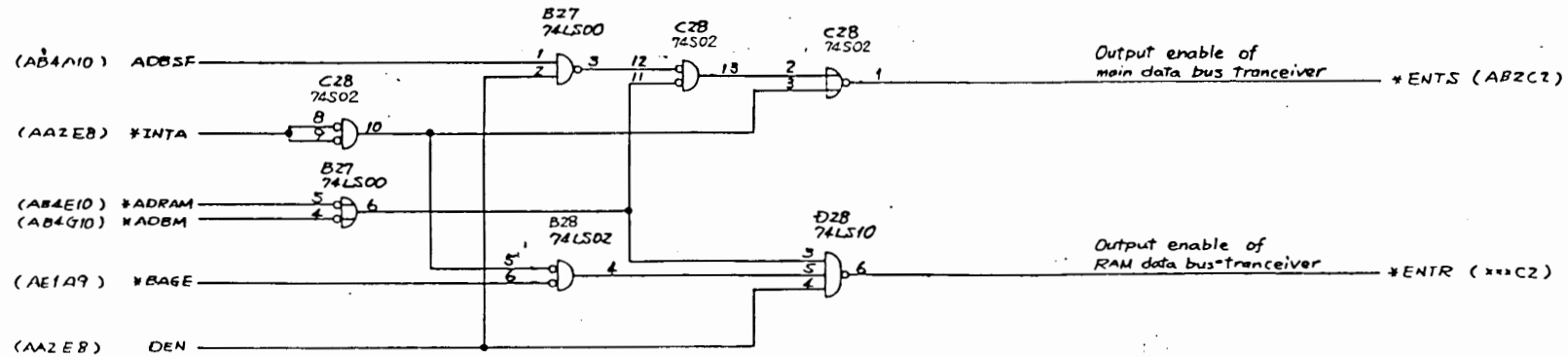
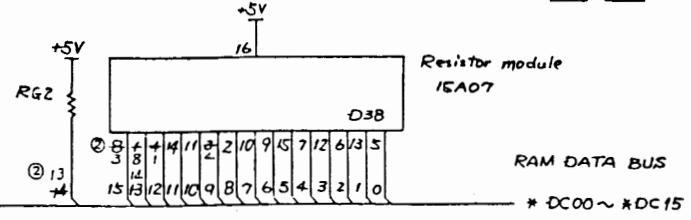
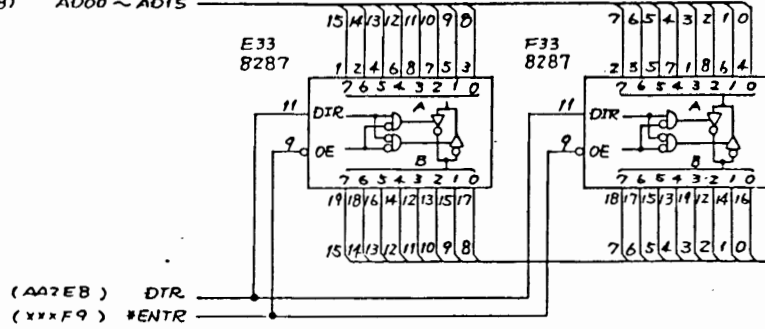
AB2

02157.6 / ZF	Modified for PCB cotton CRD.	AI6C-1000-0030 0031	101
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FW1-324299

LOCAL BUS

(AAZAB, MZB8) AD00 ~ AD15

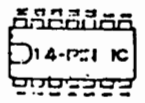
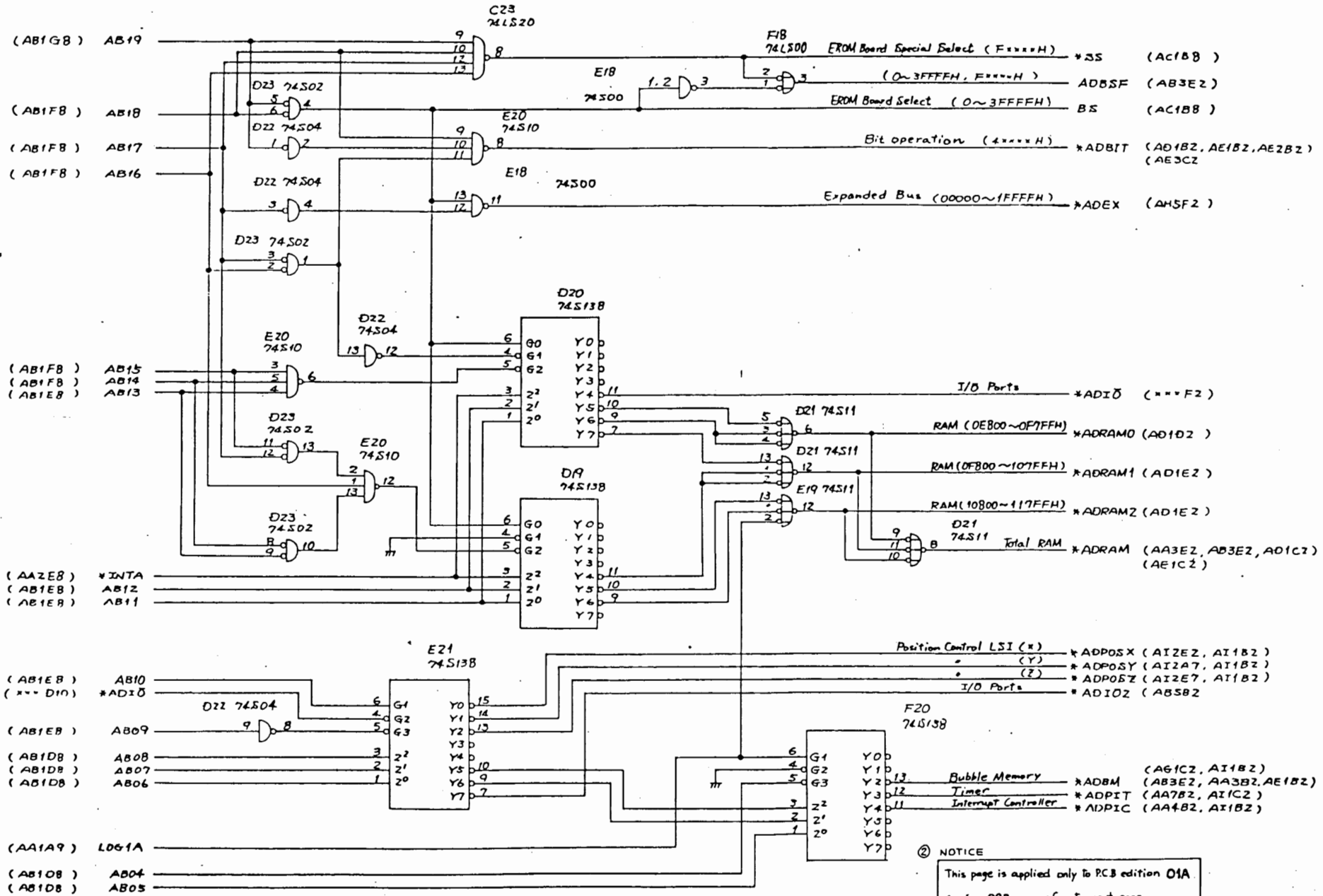


RAM DATA BUS TRANCEIVER AND OUTPUT CONTROL

REV	1	DATE	10/14/84	DESIGNED BY	...	CHECKED BY	...	APPROVED BY	...
			A16C-1000-0030						
			0031						
			1014						

A B 3

AWI-324300

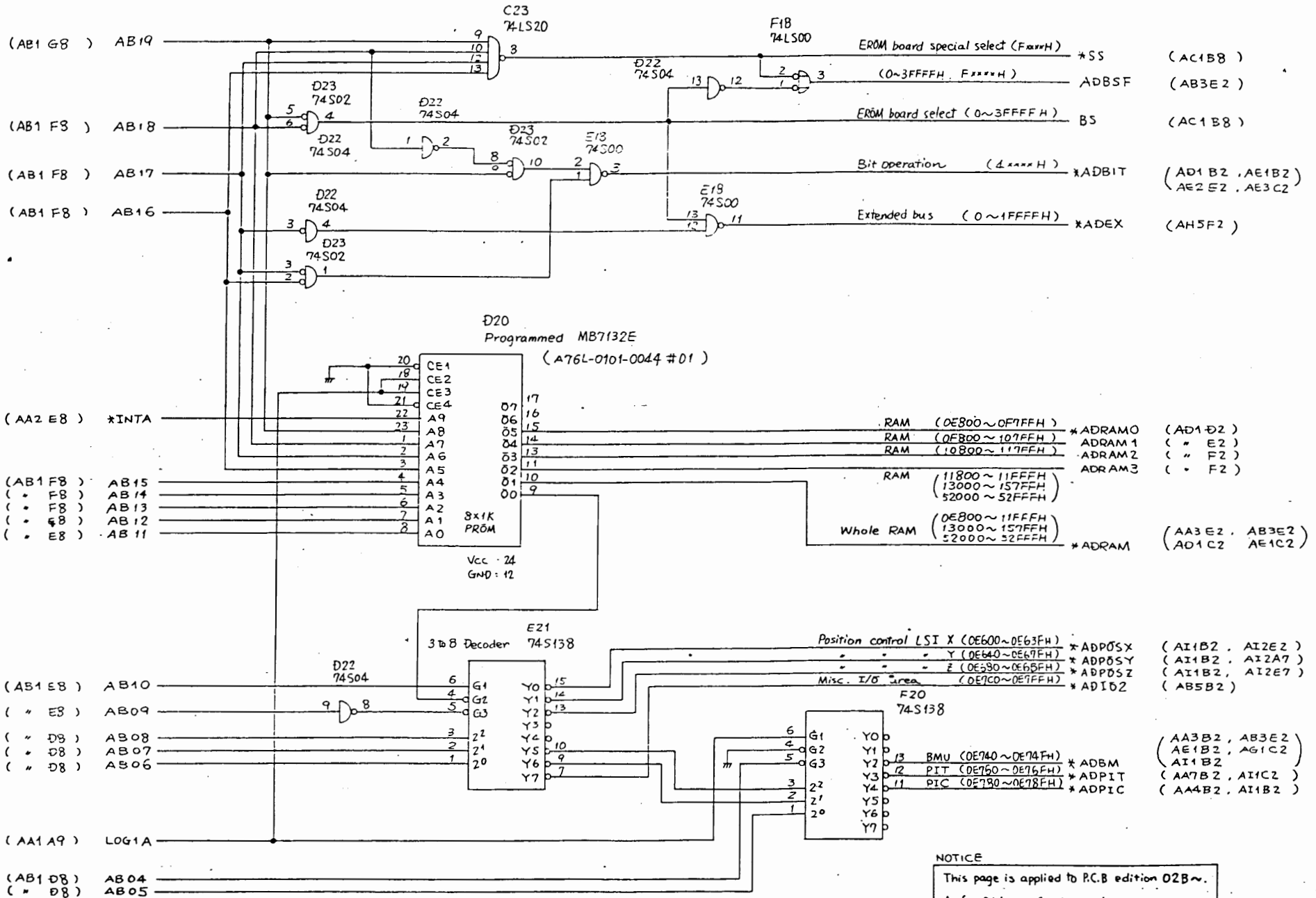


ADDRESS DECODER

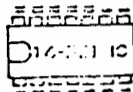
A B 4

REV	DATE	BY	CHKD	APPD	QTY	UNIT	REMARKS
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A16C-1000-0030/0031/001



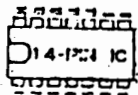
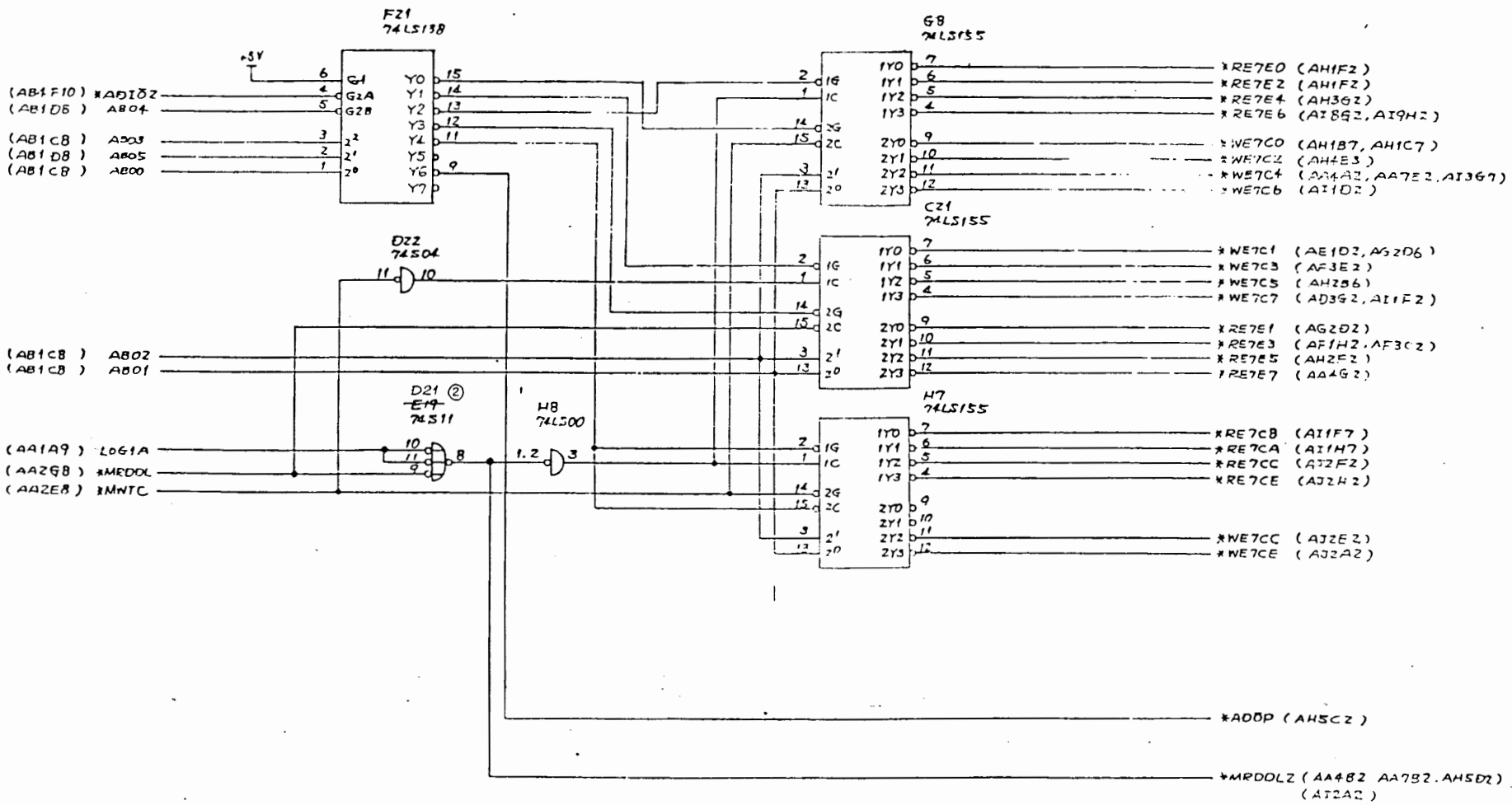
NOTICE
 This page is applied to P.C.B edition 02B~.
 As for 01A, refer to previous page.



ADDRESS DECODER

A	B	4
A16C-1000-0030		
0031		

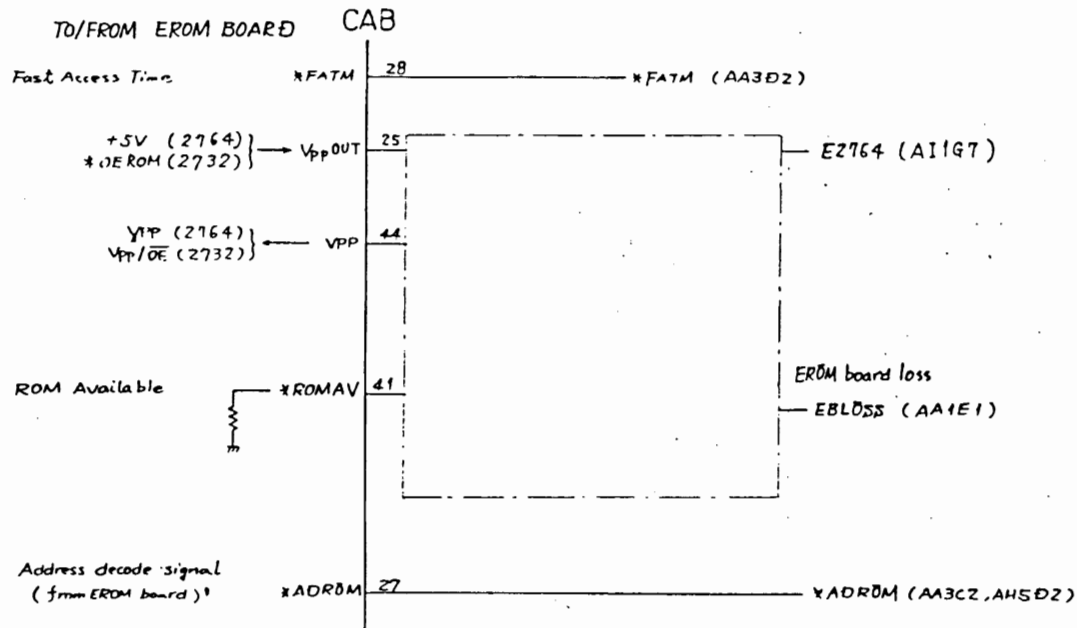
AWI-324302



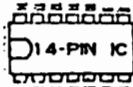
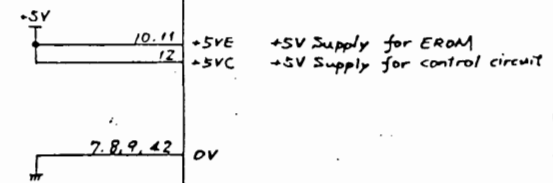
ADDRESS DECODER

		ABS	
REV			
DATE			
DESIGNED BY			
MANUFACTURED FOR			
AWI-324302	Manufactured for K5 edition 02B	A16C-1000-0030	for
		0031	

AWI-324303



CAB		TO/FROM EROM BOARD	
(AB4B10) BS	39	BS	EROM Board Select
(AB4A10) *SS	40	*SS	EROM Board Special Select
(AA2FB) STB2	24	STB	Strobe
(AH5D10) *DTR	26	*DTR	Data Transmit/Receive
(AH5B10) *A01X	1	*A01X	Address
() *A02X	33	*A02X	
() *A03X	2	*A03X	
() *A04X	34	*A04X	
() *A05X	3	*A05X	
() *A06X	19	*A06X	
() *A07X	35	*A07X	
() *A08X	4	*A08X	
() *A09X	20	*A09X	
() *A10X	36	*A10X	
() *A11X	5	*A11X	
() *A12X	21	*A12X	
() *A13X	37	*A13X	
() *A14X	6	*A14X	
() *A15X	22	*A15X	
() *A16X	38	*A16X	
() *A17X	23	*A17X	
(AB2C6) *DB00	29	*DB00	Data
() *DB01	13	*DB01	
() *DB02	45	*DB02	
() *DB03	14	*DB03	
() *DB04	30	*DB04	
() *DB05	46	*DB05	
() *DB06	15	*DB06	
() *DB07	31	*DB07	
() *DB08	47	*DB08	
() *DB09	16	*DB09	
() *DB10	32	*DB10	
() *DB11	48	*DB11	
() *DB12	17	*DB12	
() *DB13	49	*DB13	
() *DB14	18	*DB14	
(AB2F6) *DB15	50	*DB15	

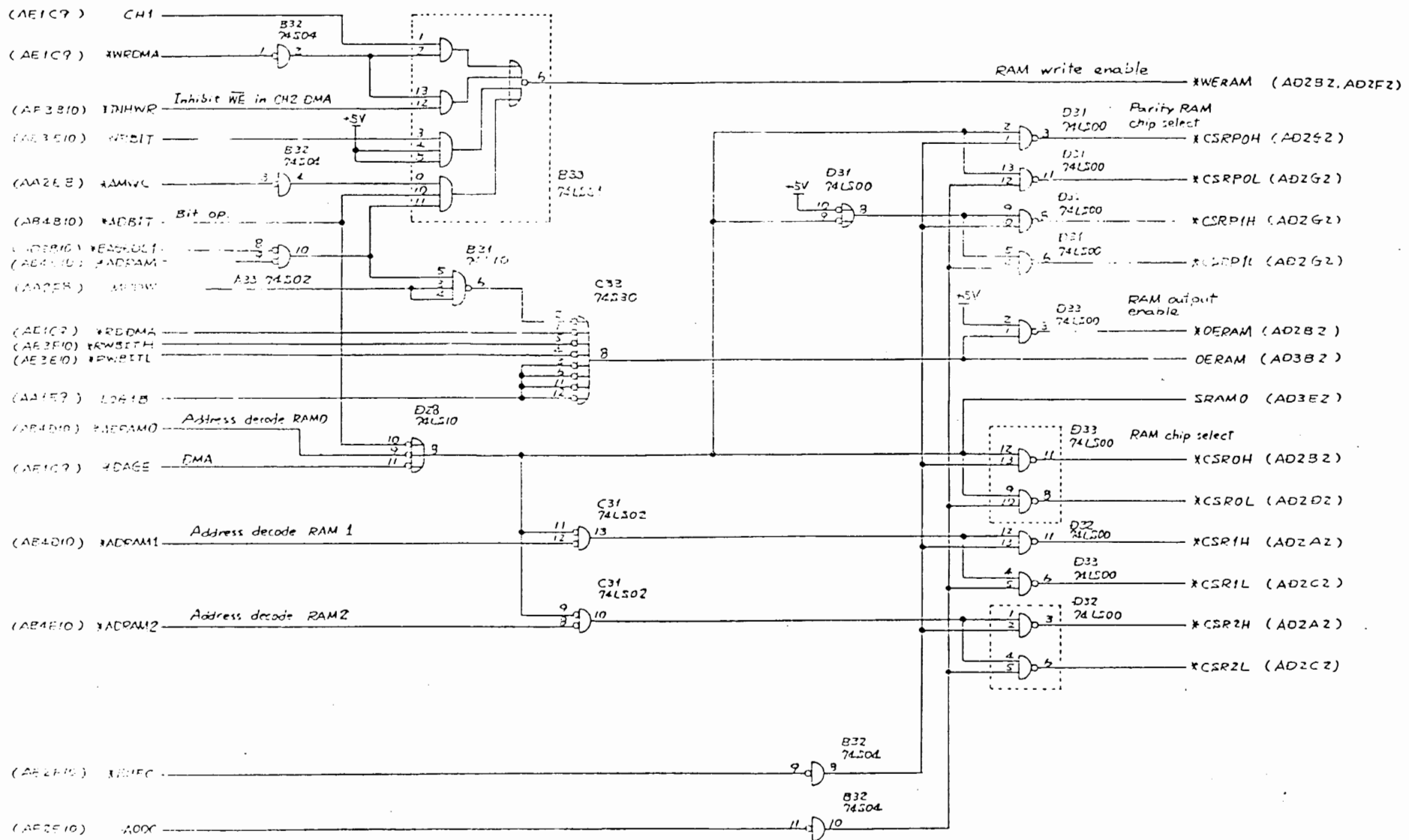


ROM BOARD INTERFACE

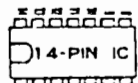
AWI-324303	AWI-324303	AWI-324303	AWI-324303
AWI-324303	AWI-324303	AWI-324303	AWI-324303
AWI-324303	AWI-324303	AWI-324303	AWI-324303
AWI-324303	AWI-324303	AWI-324303	AWI-324303

AWI-324303

AW1324304



② NOTICE
 This page is applied only to PCS edition 01A.
 As for 02B~, refer to next page.

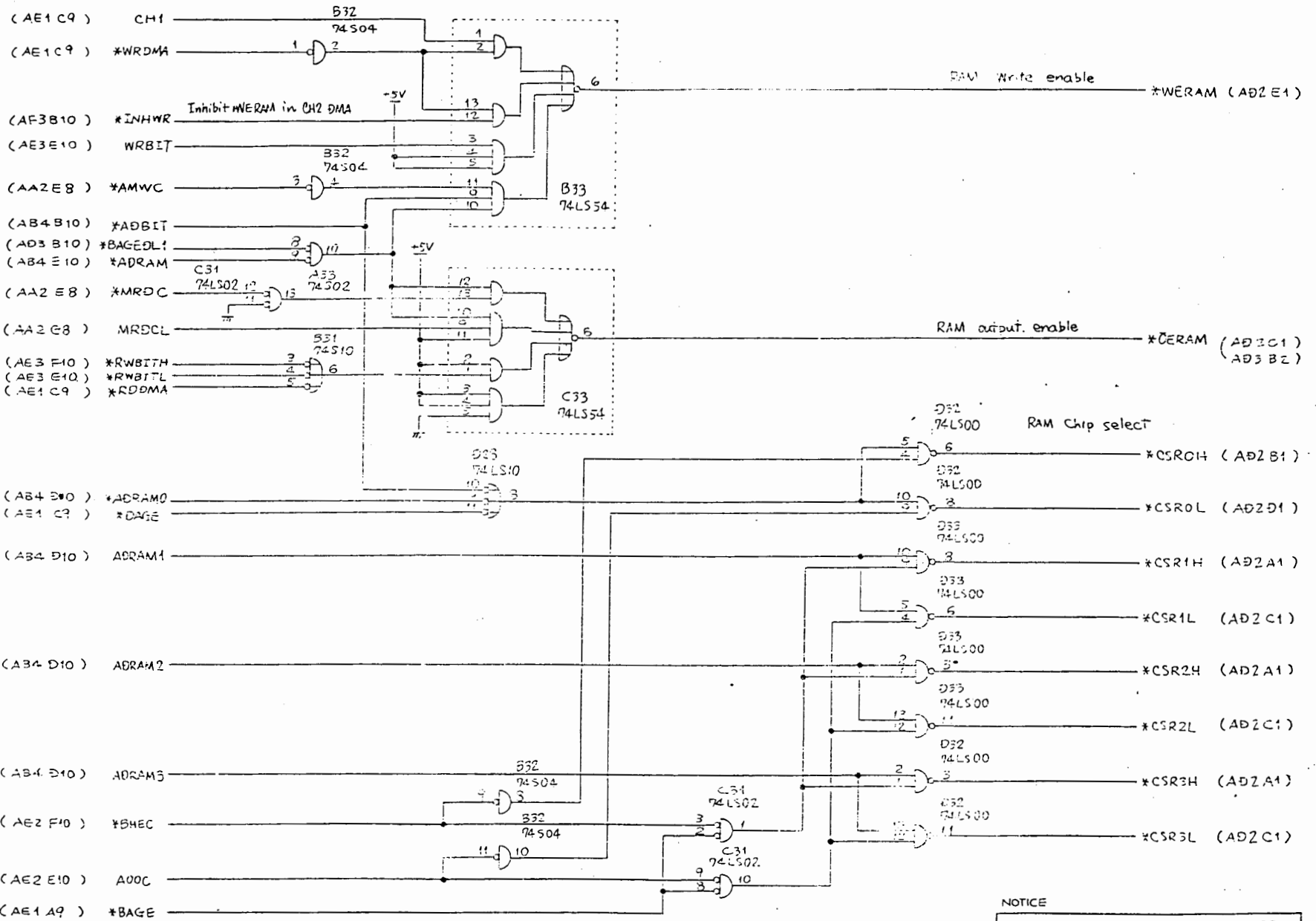


RAM READ WRITE CONTROL

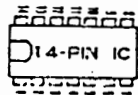
AD 1

AI6C-1000-0030/01

AWI-324305



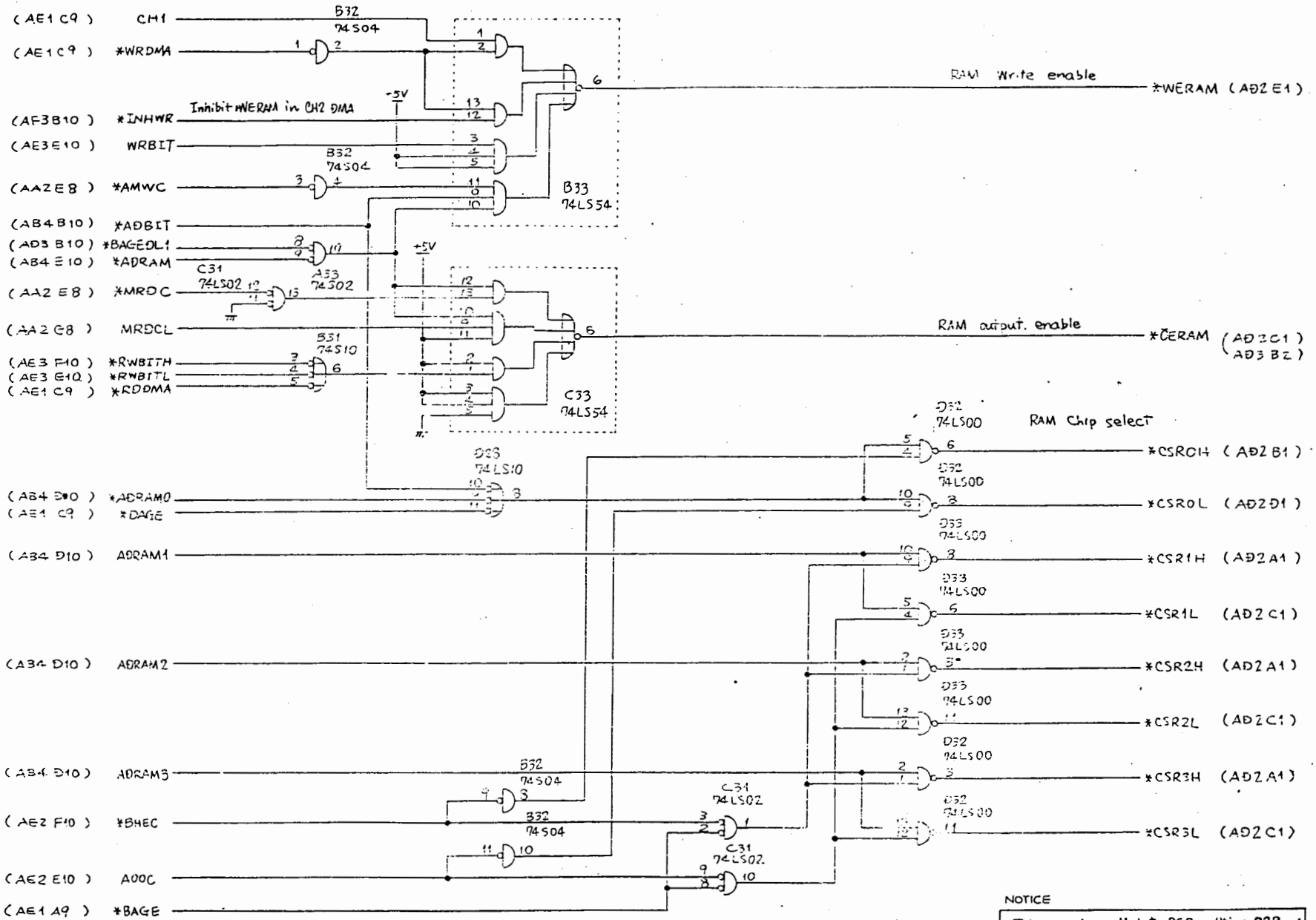
NOTICE
 This page is applied to PCB edition 02B.
 As for 01A, refer to previous page.



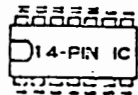
RAM READ WRITE CONTROL

02157.5.11.004	This page is inserted from PCB edition 02B.	A16C-1000-0030	0031	101
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A D 1

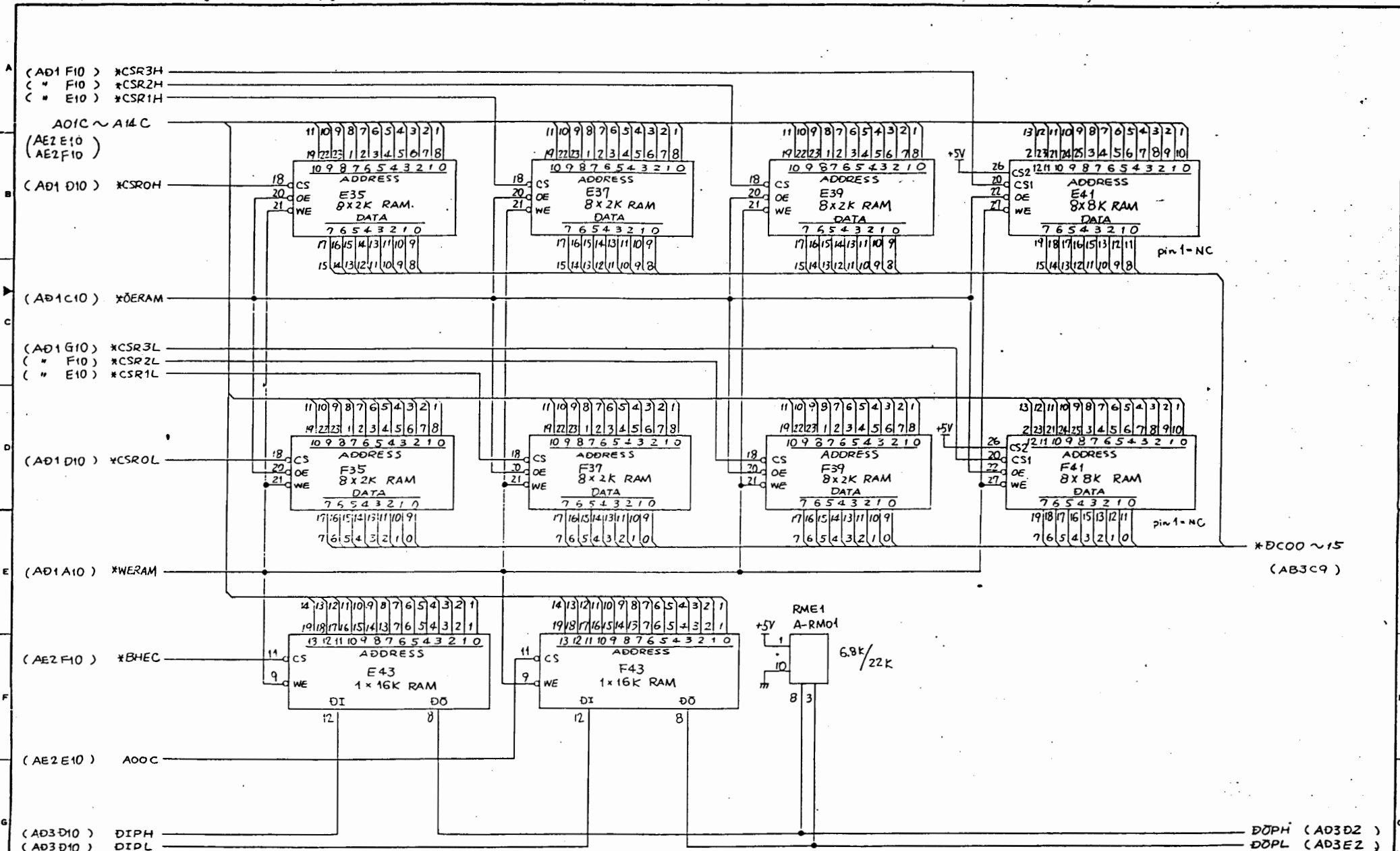


NOTICE
This page is applied to PCB edition 02B~.
As for 01A, refer to previous page.



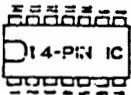
RAM READ WRITE CONTROL

AWI-324305	14-PIN IC	RAM READ WRITE CONTROL	AWI-324305	14-PIN IC	RAM READ WRITE CONTROL
02151.6.11.24	This page is inserted for PCB edition 02B.	A16C-1000-0030	0031	101	AD 1



(HITACHI)

- 8x 8K RAM : HM6264P-15
- 8x 2K RAM : HM6116P-3
- 1x 16K RAM : HM6167P-8



28K BYTE RAM WITH PARITY BIT

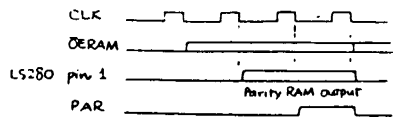
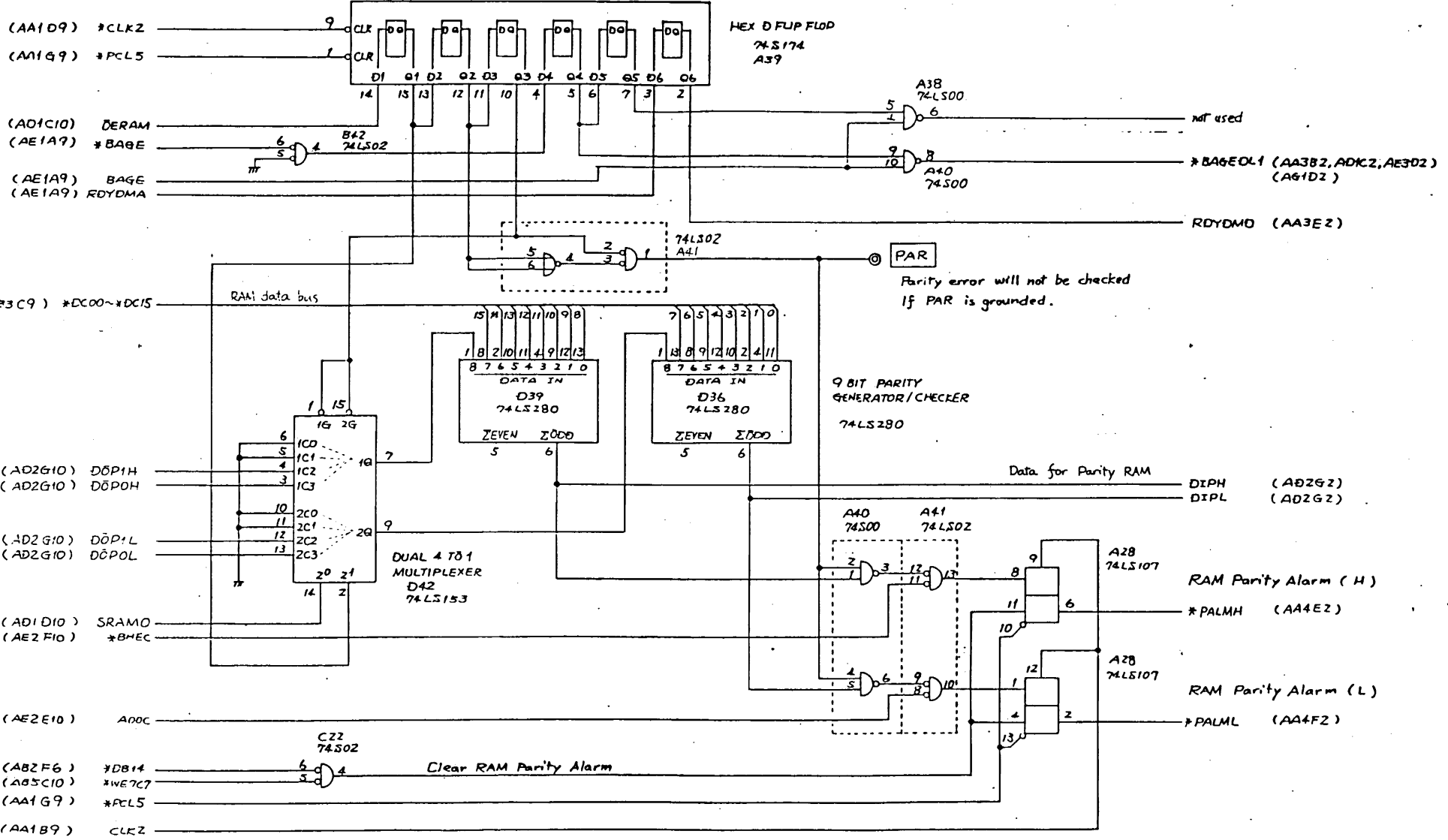
NOTICE

This page is applied To PCB edition 02B~.
As for 01A , refer To previous page.

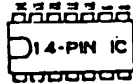
A D 2

AWI-324307	Rev. 1.0	1987.6.1	1987.6.1	0030	0031
This page is inserted for PCB edition 02B			A16C-1000-0030/01		

AW1-32130B



Parity check timing



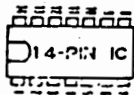
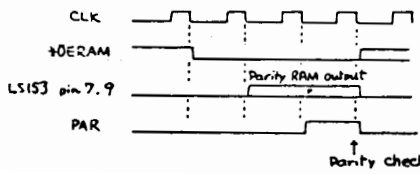
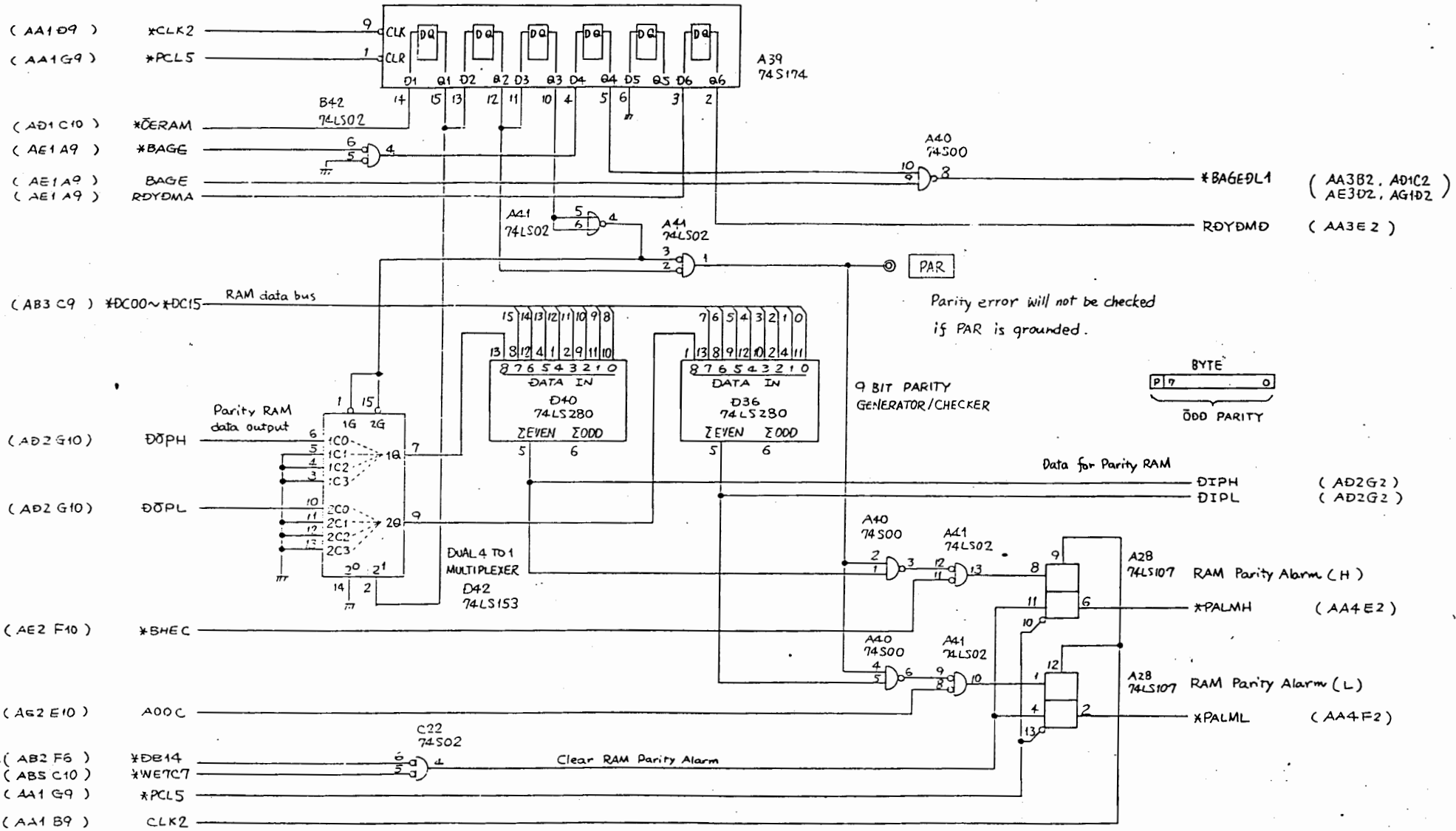
PARITY GENERATOR CHECKER

NOTICE
 This page is applied only to PCB edition 01A.
 As for 02B~, refer to next page.

AD3

AW1-32130B	REV. 1	DATE	BY
AW1-32130B	REV. 1	DATE	BY
AW1-32130B	REV. 1	DATE	BY
AW1-32130B	REV. 1	DATE	BY

1161676.1 12.81 NOTICE is required A16C-1000-0030/0031/001



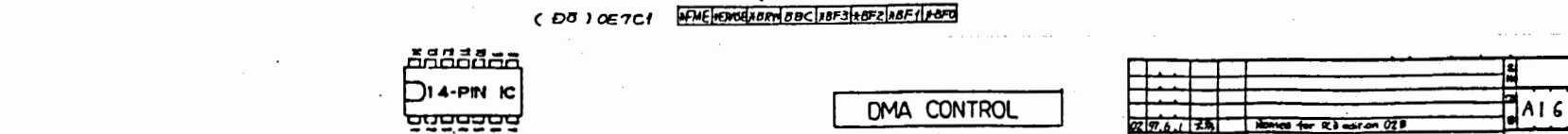
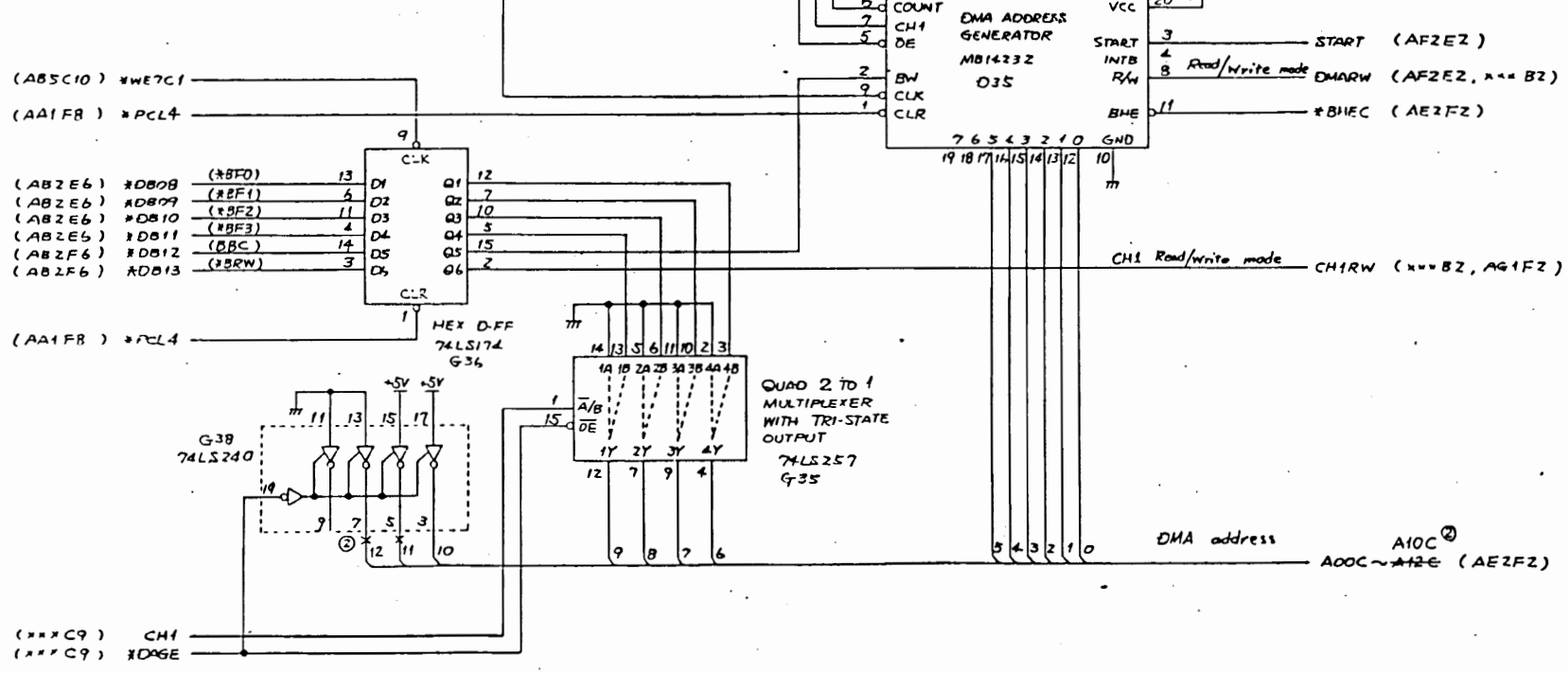
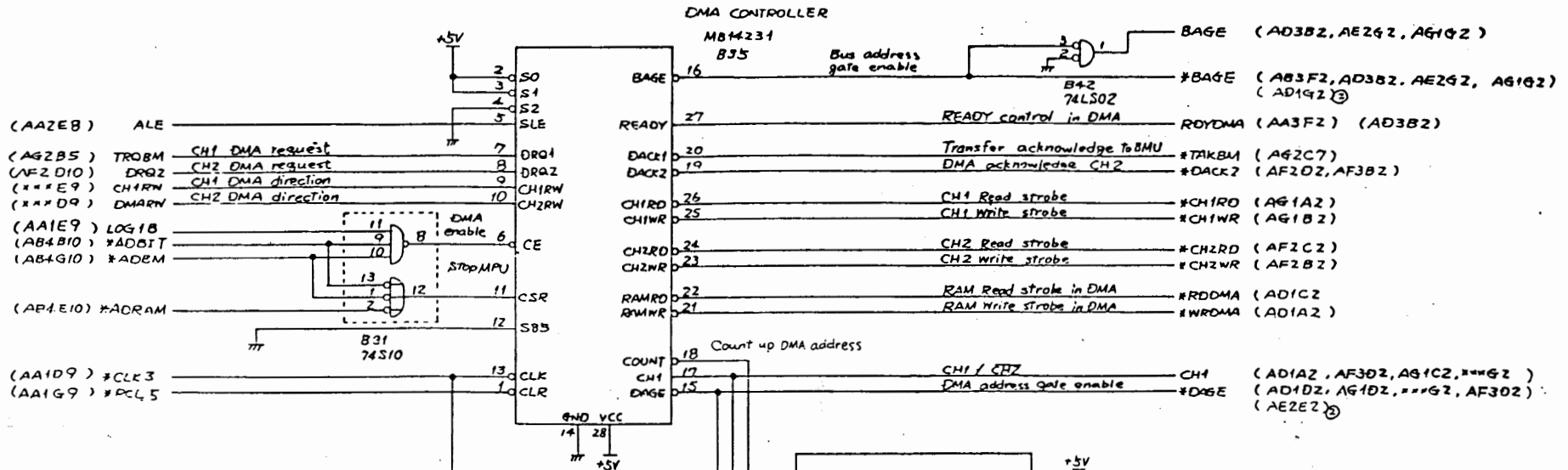
NOTICE
 This page is applied to P.C.B edition 02B.
 As for 01A, refer to previous page.

A D 3

02/06/1	0030	0031	101
This page is reserved for PCB edition 02B.			A16C-1000-0030
			0031

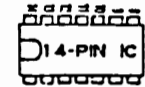
48
99

AW1-324310



49
75

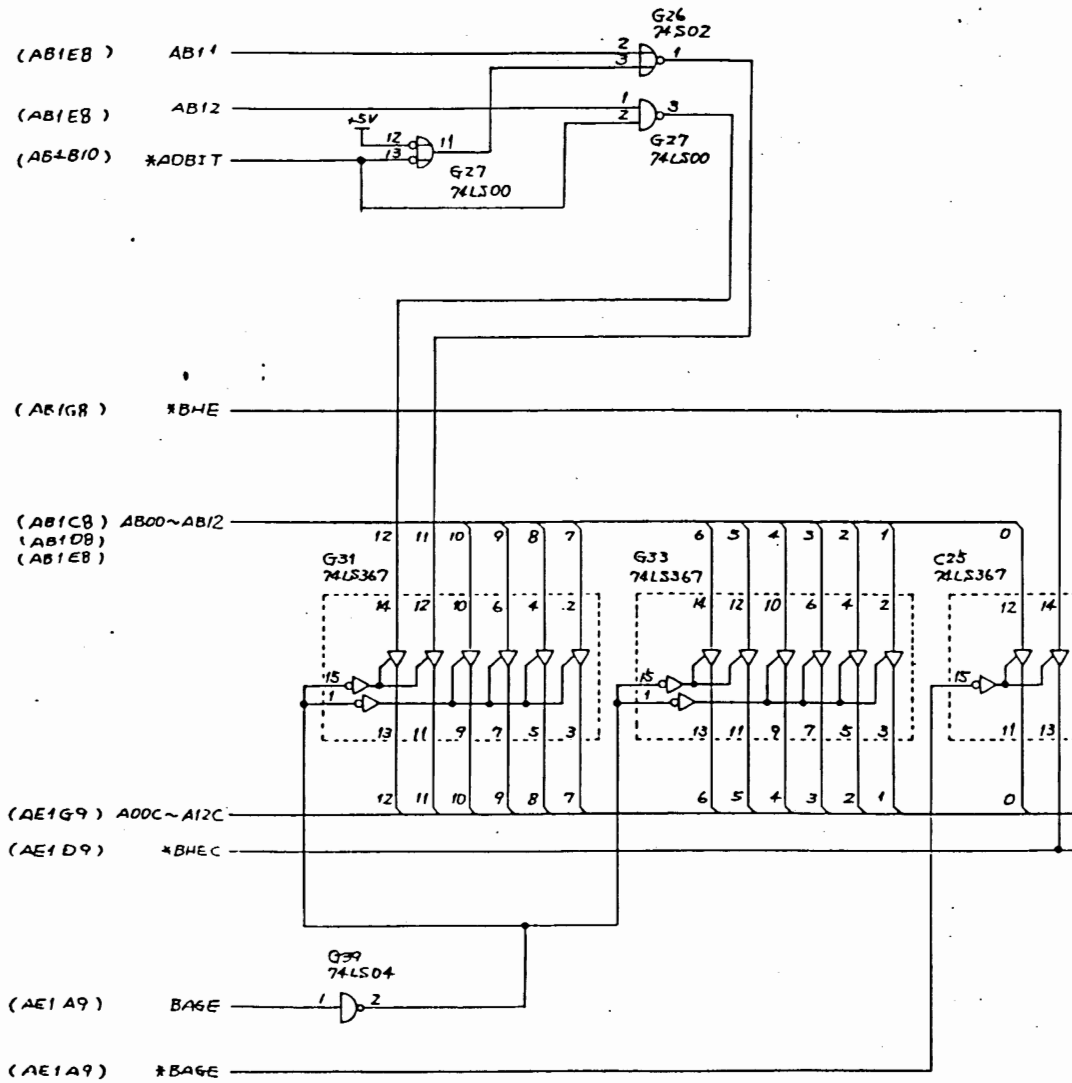
(DB) OE7C1 0FME 0EN08ABRW B0C 1BF3+BF2 1BF1 1BFD



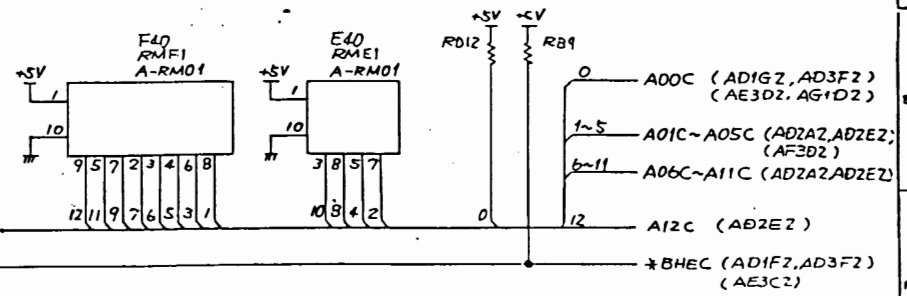
DMA CONTROL

Q1	Q2	Q3	Q4	Q5	Q6	Q7	Q8	Q9	Q10

AWI 324311

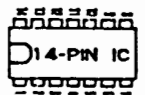


	OPERATION MODE		
	Normal Operation	Bit Operation	DMA Operation
*BHEC	*BHE	*BHE	*BHE (DMA)
A00C~A10C	AB00~AB10	AB00~AB10	A00~A10 (DMA)
A11C	$\overline{AB11}$	0	0
A12C	$\overline{AB12}$	1	1



NOTICE

② This page is applied only to PCB edition 01A.
As for 02B~, refer to next page.



ADDRESS BUFFER FOR RAM

A E 2

DATE	REV	DESCRIPTION	BY
02/17/61	1	NOTICE is added	
02/18/61	2		
02/21/61	3		
02/24/61	4		
02/27/61	5		
03/01/61	6		
03/04/61	7		
03/07/61	8		
03/10/61	9		
03/13/61	10		
03/16/61	11		
03/19/61	12		
03/22/61	13		
03/25/61	14		
03/28/61	15		
03/31/61	16		
04/03/61	17		
04/06/61	18		
04/09/61	19		
04/12/61	20		
04/15/61	21		
04/18/61	22		
04/21/61	23		
04/24/61	24		
04/27/61	25		
04/30/61	26		
05/03/61	27		
05/06/61	28		
05/09/61	29		
05/12/61	30		
05/15/61	31		
05/18/61	32		
05/21/61	33		
05/24/61	34		
05/27/61	35		
05/30/61	36		
06/02/61	37		
06/05/61	38		
06/08/61	39		
06/11/61	40		
06/14/61	41		
06/17/61	42		
06/20/61	43		
06/23/61	44		
06/26/61	45		
06/29/61	46		
07/02/61	47		
07/05/61	48		
07/08/61	49		
07/11/61	50		
07/14/61	51		
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07/20/61	53		
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09/21/61	74		
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10/12/61	81		
10/15/61	82		
10/18/61	83		
10/21/61	84		
10/24/61	85		
10/27/61	86		
10/30/61	87		
11/02/61	88		
11/05/61	89		
11/08/61	90		
11/11/61	91		
11/14/61	92		
11/17/61	93		
11/20/61	94		
11/23/61	95		
11/26/61	96		
11/29/61	97		
12/02/61	98		
12/05/61	99		
12/08/61	100		

(AB1E8) AB12~AB14
(AB1F8) (AB1F8)

(AB3C9) *DC00~*DC15

(AEZF10) *BHEC

(AB4B10) *ADB1T

(AEZE10) A00C

(AD3B10) *BAGEDL

(AB1F9) AB15

(AAZEB) *MR9C

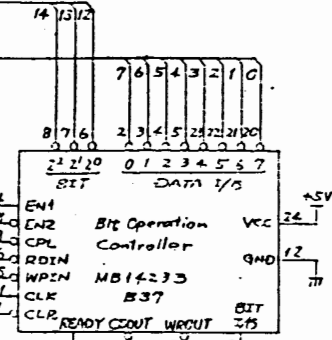
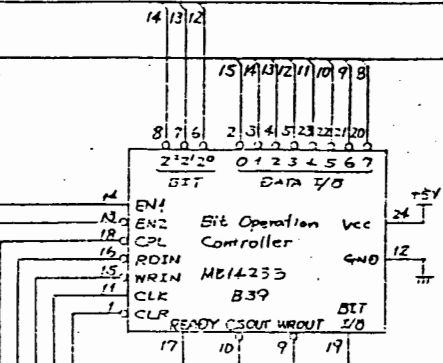
(AAZFB) *AMWC

(AAIC9) CLK3

(AAIG9) *PCL5

B42 74LS02

B42 74LS02



MPU local bus bit7 A007 (AAZB8)

A38 74LS00

Write strobe WRBIT (AD1B2)

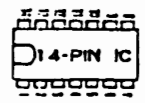
RAM chip select control *RNB1TL (AD1C2)

READY control in bit operation ROYBTL (AA3G2)

MPU local bus bit15 AD15 (AAZB8)

*RNB1TH (AD1C2)

ROYBTH (AA3F2)

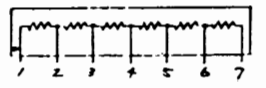


BIT OPERATION

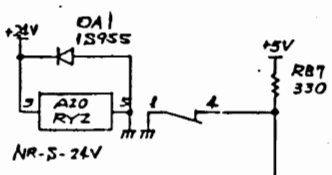
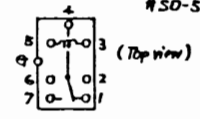
A E 3	
AW1-324313	16C-1000-0030/01
0031	15028

AV11-324314

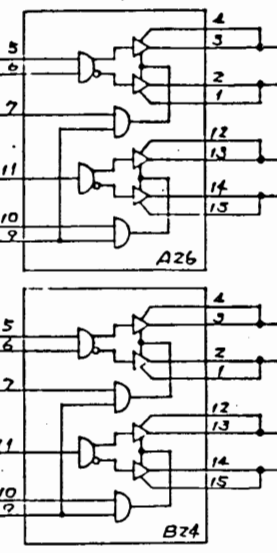
Resistor module
A76L-0202-0003



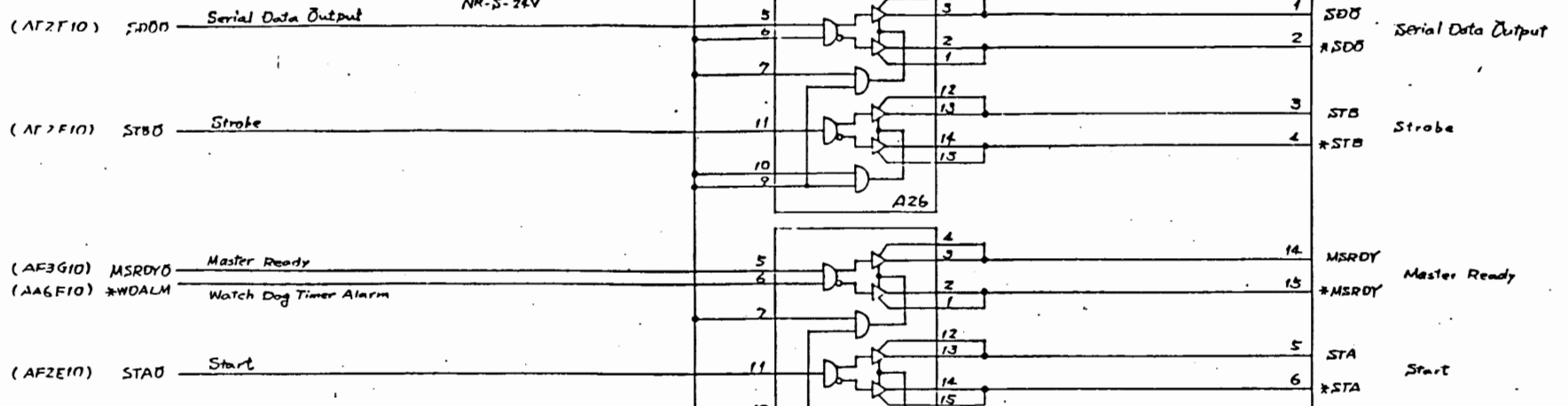
NR-series reed relay
ASBL-0001-0071 NS-24V
#SD-5V



DUAL DIFFERENTIAL
LINE DRIVERS
75113



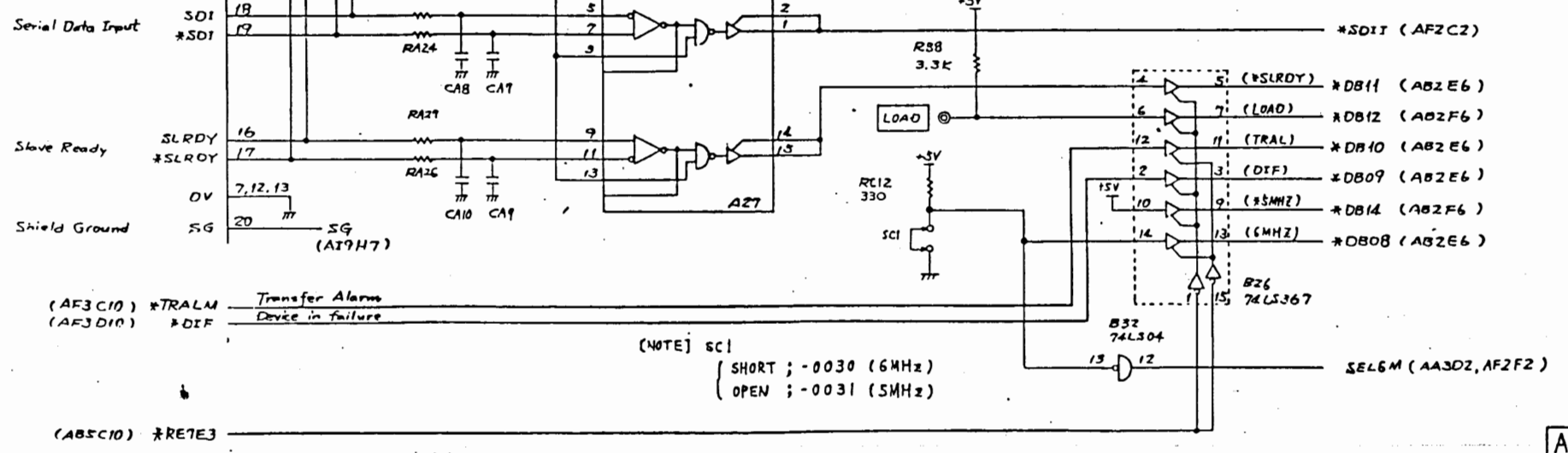
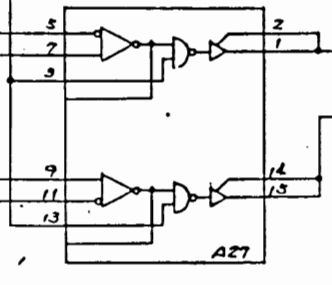
CADX TO SLAVE UNITS



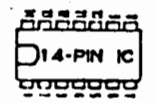
FROM SLAVE UNITS
- MDT&CRT
- CONNECTION UNIT
- PC model C

CADX

DUAL DIFFERENTIAL
LINE RECEIVERS
75115



[NOTE] sc1
{ SHORT ; -0030 (6MHz)
{ OPEN ; -0031 (5MHz)



SERIAL I/O INTERFACE

Table with 4 columns and 10 rows, likely a pinout or component list table.

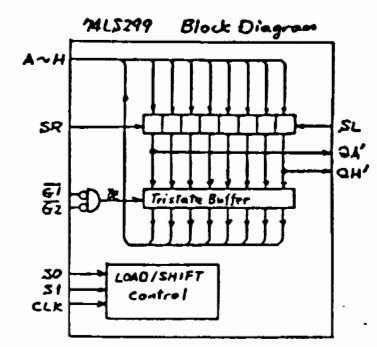
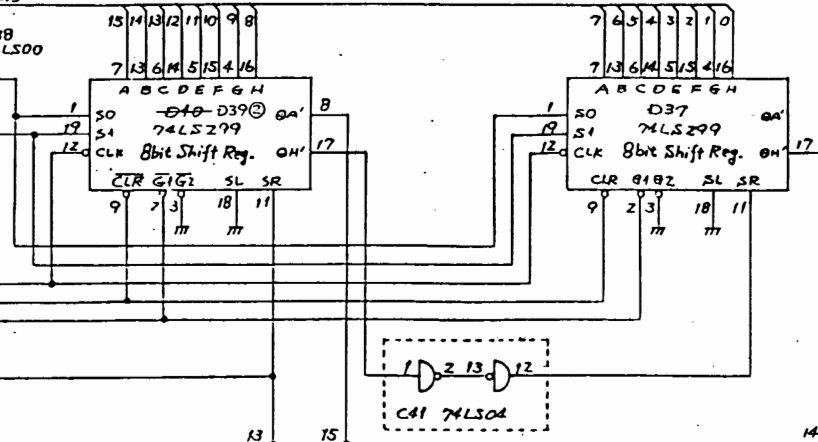
A16C-1000-0030/0031

A F I

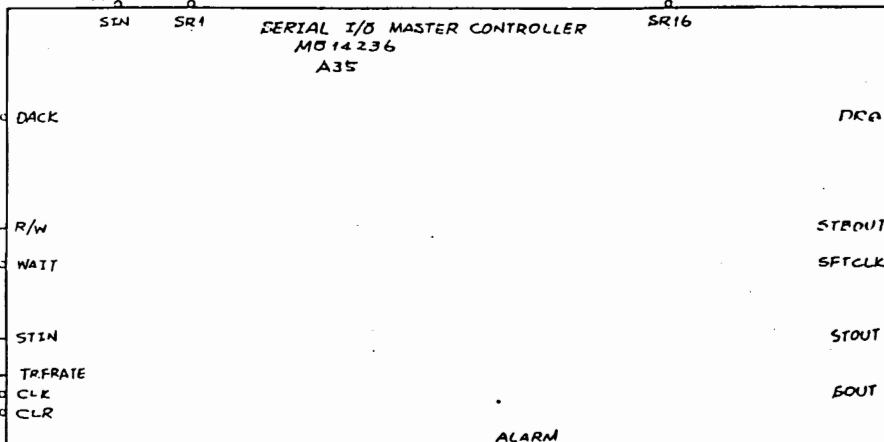
2WL324315

(AF3 E10) TRSTA Transfer Start
(AB3C9) *DC00~*DC15 RAM data bus

(***H11) *SFTEN
(AE1 B9) *CH2WR
(AA1 D9) *CLK3
(AA1 F9) *PCL4
(AE1 B9) *CH2RD
(AF1 E10) *S011

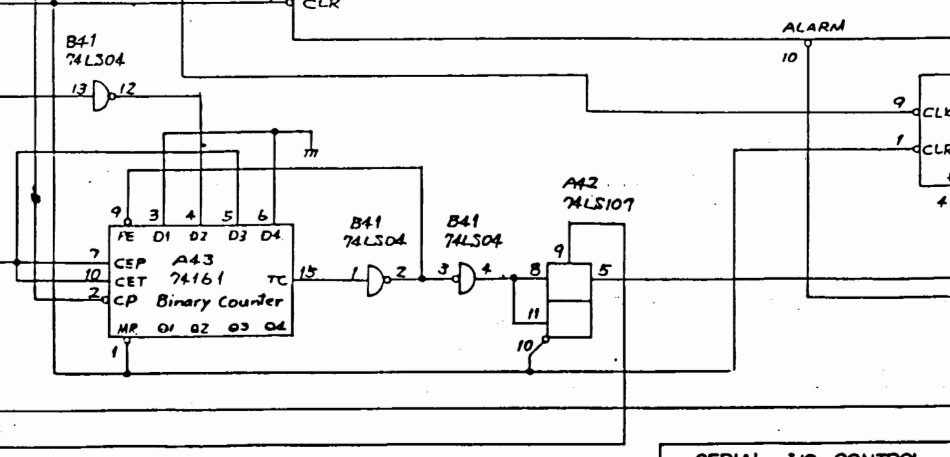


(AE1 B9) *DACK2 DMA Acknowledge (CH2)
(AE1 D9) DMARN DMA Read/Write
(AF3 G10) *TRWAIT Transfer wait
(AA1 D9) *CLK3
(AE1 D9) START
(***G11) C250K
(AA1 D9) *CLK2
(AA1 G9) *PCL5



*DRQ2 (AF3A2)
DMA Request (CH2)
DRQ2 (AE1B2)
Strobe STB0 (AF1B2)
Shift Clock SFTCLK (**H2)
Start of Serial I/O cycle STA0 (AF1D2)
Serial Data Output SDB0 (AF1B2)

(AF1 G10) SEL6M
(AF3 E10) LOGIC
(***E11) SFTCLK
(AA1 C9) CLK3



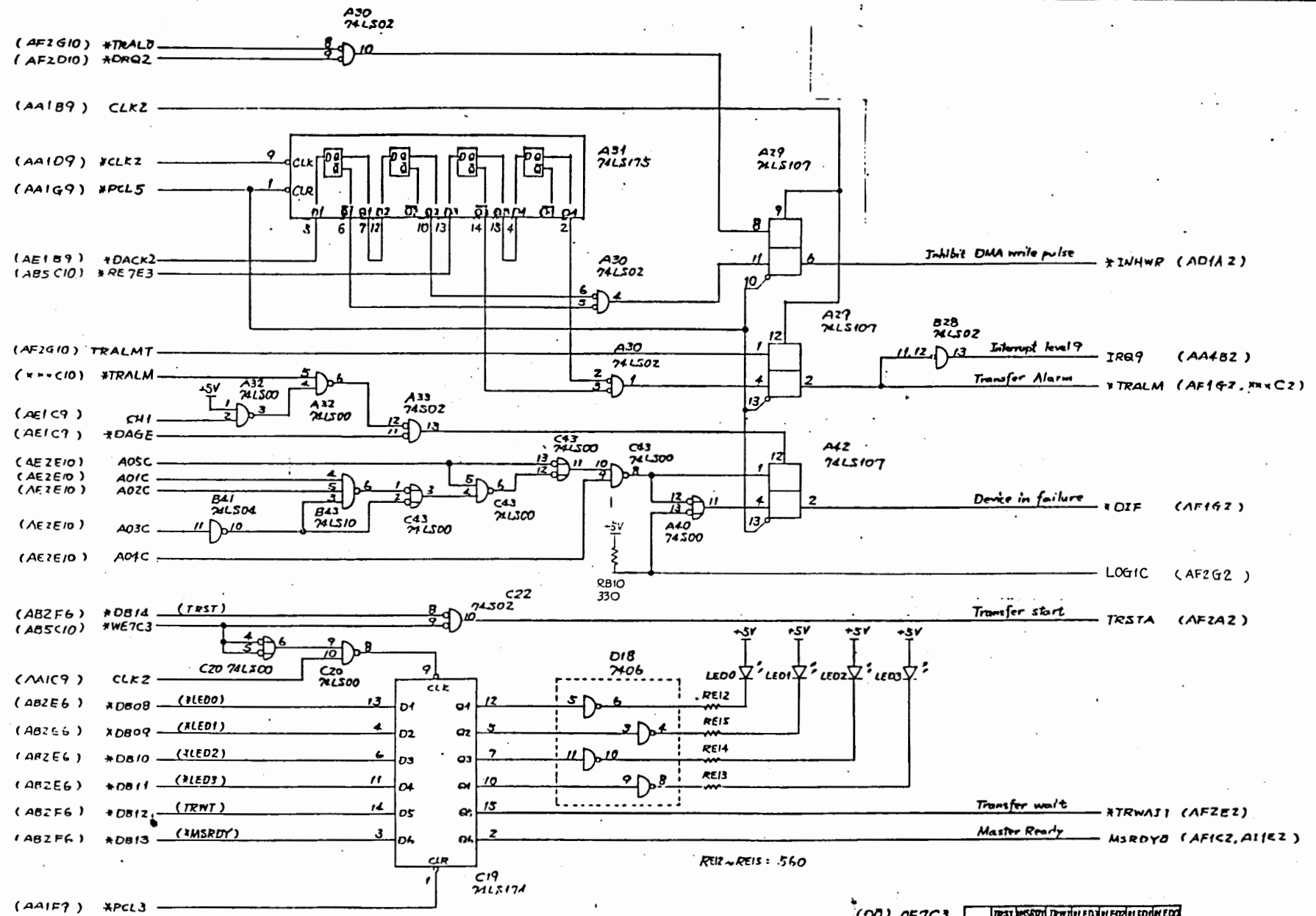
QUAD D FLIP-FLOP 74LS175 A37
A30 74LS02
TRALMT (AF3C2)
250KHz C250K (**E2)
*TRALO (AF3A2)
Shift enable *SFTEN (**A2)

SERIAL I/O CONTROL

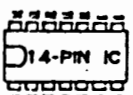
AF 2

Table with columns for revision, date, and other tracking information. Includes a reference number A16C-1000-0039/01.

AWI-32431A



(00) 0E7C3 [TRST #MSRDY #TRWAI #LED3 #LED2 #LED1 #LED0]



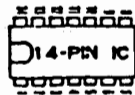
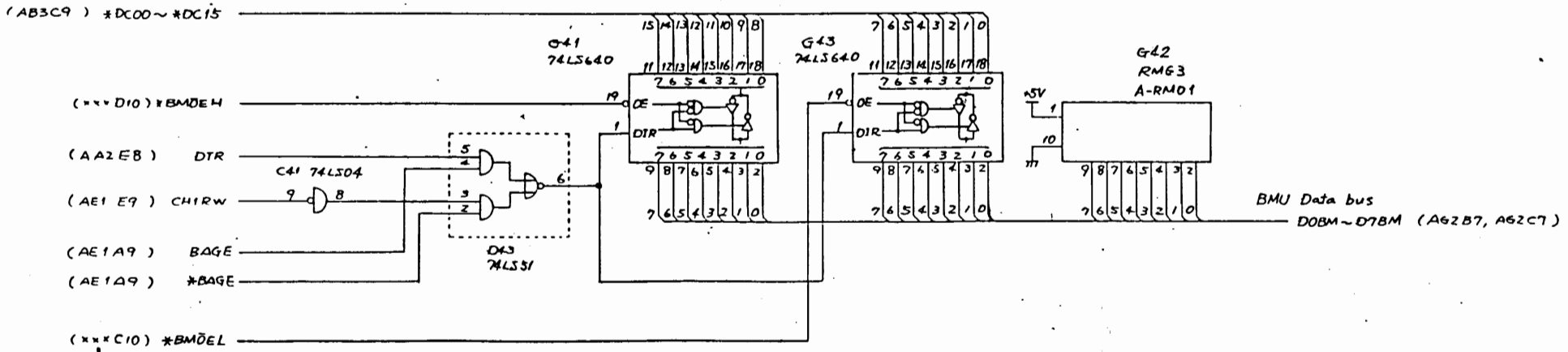
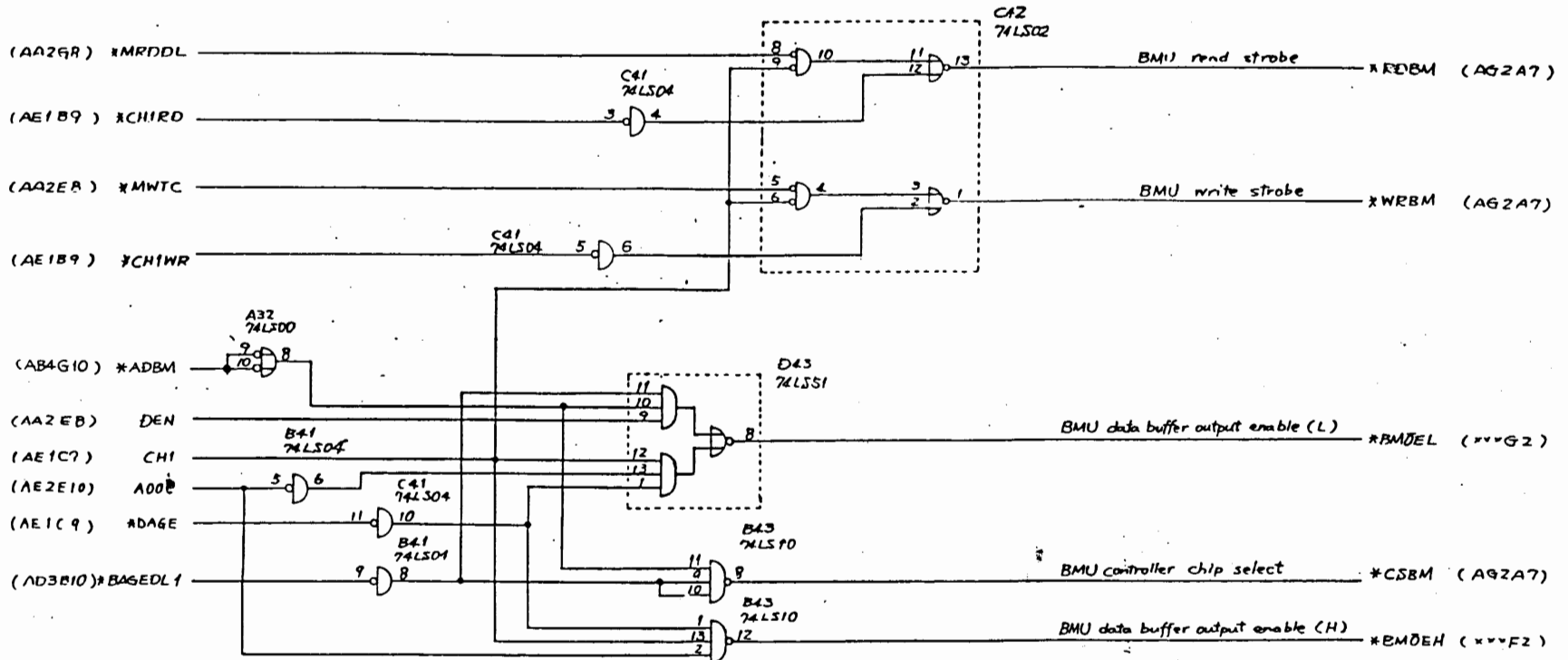
TRANSFER ALARM LOGIC AND
LED DISPLAY

1	2	3	4	5	6	7	8	9	10	11	12	13	14

AF3

116C-1000-0030 / 01

AWI-324317

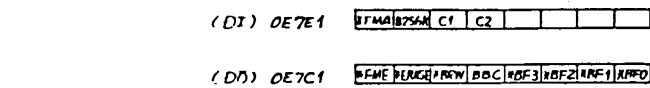
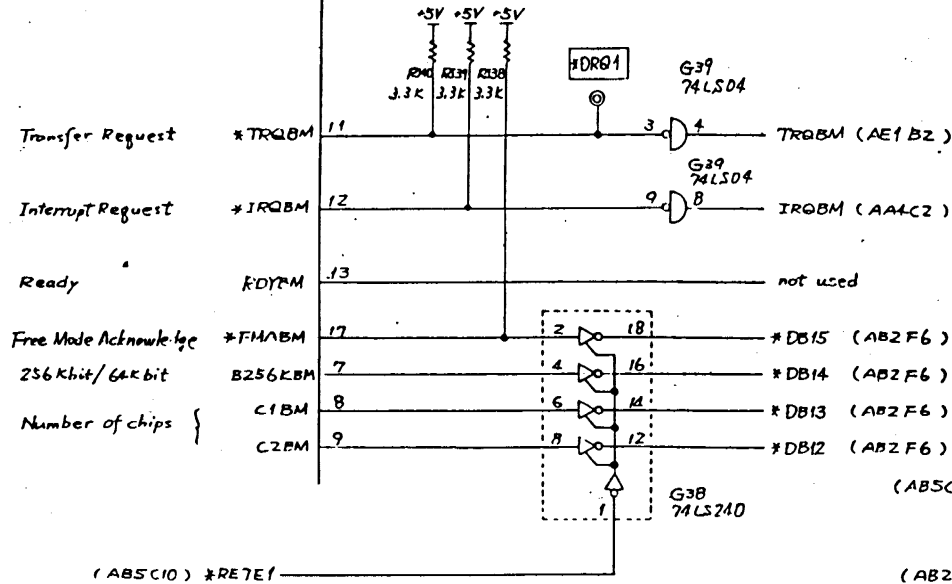


BUBBLE MEMORY UNIT INTERFACE

AWI		AGI	
A16C-1000-0030		0031	
0032		0031	

AWI-32431B

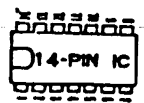
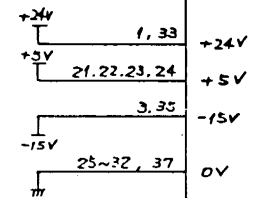
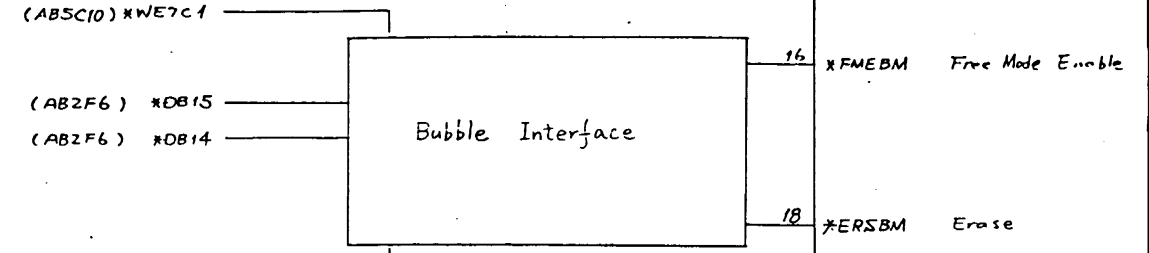
FROM BUBBLE MEMORY CABM



CABM

TO BUBBLE MEMORY (BMU)

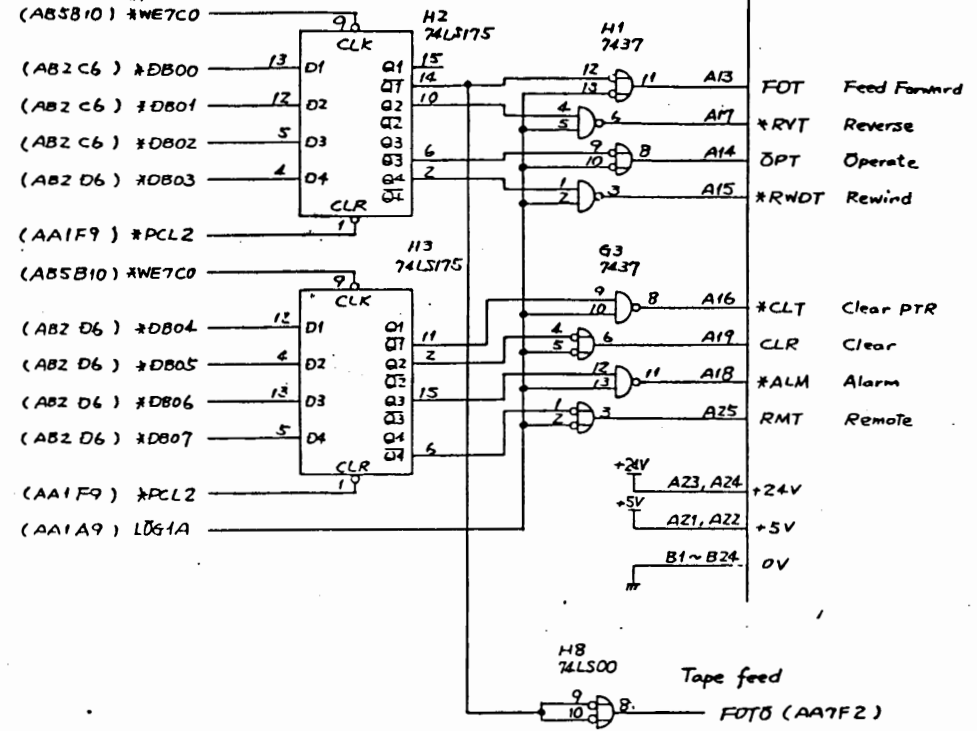
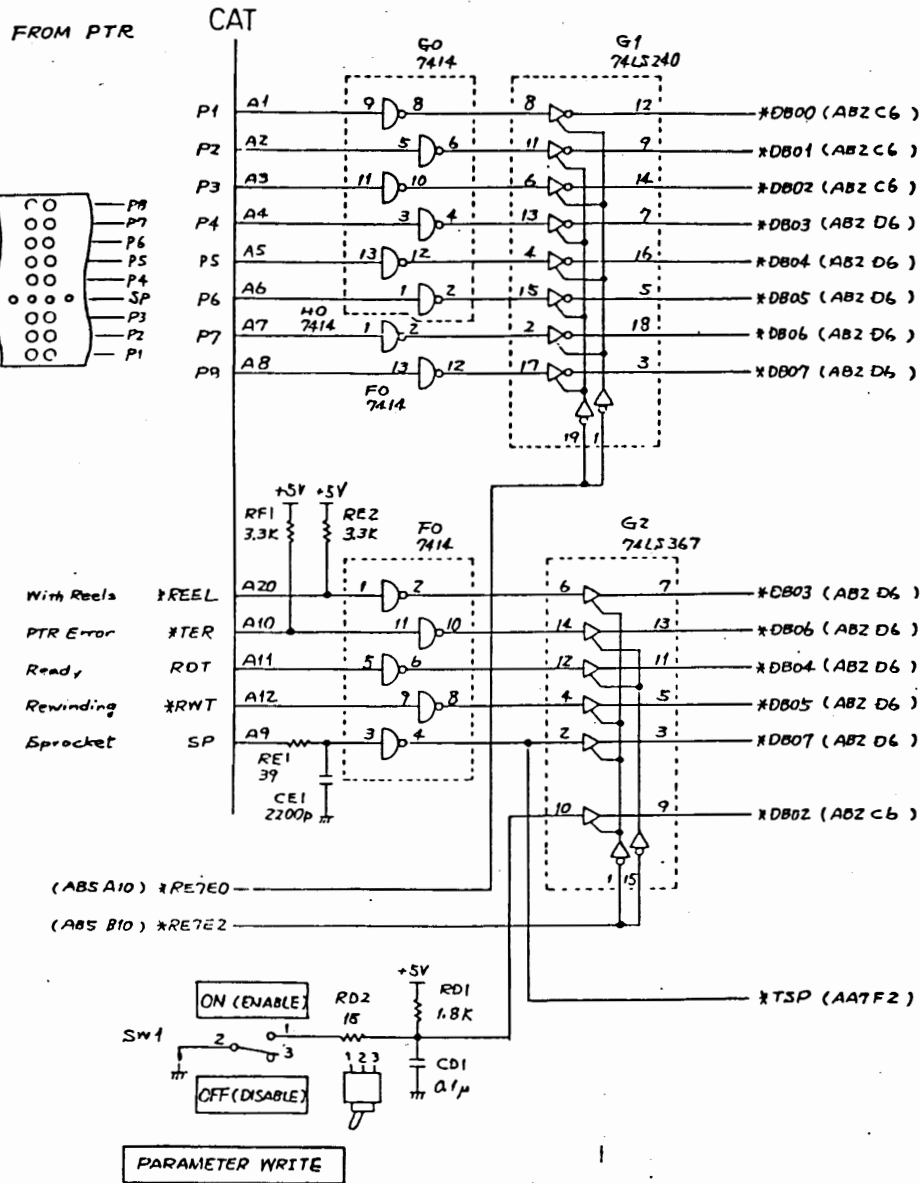
(AG1D10) *CSBM	38	*CSBM	Chip Select
(AG1A10) *RQBM	15	*RQBM	Read Strobe
(AG1B10) *WRBM	14	*WRBM	Write Strobe
(AH5B10) *A01X	41	RS0BM	Register Select (Address)
(AH5B10) *A02X	40	RS1BM	
(AH5B10) *A03X	39	RS2BM	
(AG1F10) DOBM	43	DOBM	Data
() D1BM	44	D1BM	
() D2BM	45	D2BM	
() D3BM	46	D3BM	
() D4BM	47	D4BM	
() D5BM	48	D5BM	
() D6BM	49	D6BM	
(AG1F10) D7BM	50	D7BM	
(AA5F5) ENI	5	*PDQBM	Power Down
(AA1E9) *PCL0	6	*PQKBM	Power on reset
(AE1B9) *TAKBM	10	*TAKBM	Transfer Acknowledge



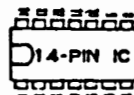
BUBBLE MEMORY UNIT INTERFACE

AGZ	
A16C-1000-0030/0031 Vol	

AWL-324319



- (D1) 0E7E0 [P8 P7 P6 P5 P4 P3 P2 P1]
- (D1) 0E7E2 [SP TER RWT RDT REEL PWE]
- (D0) 0E7C0 [PTR ALM CLR CLT RWDT OPT RYT RDT]

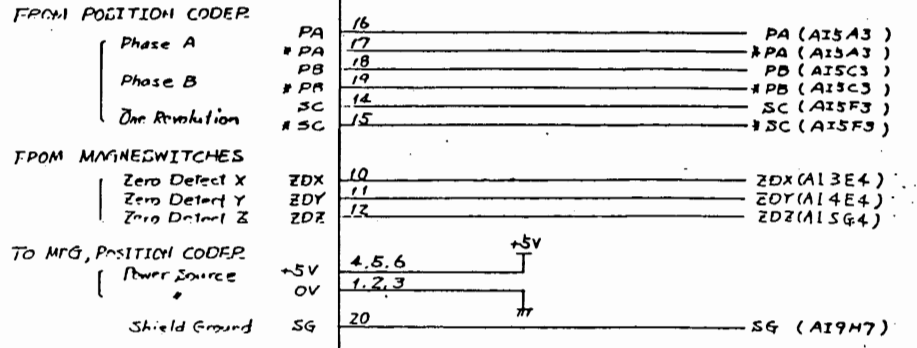


PAPER TAPE READER INTERFACE

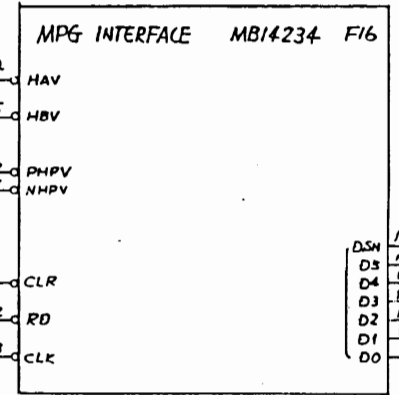
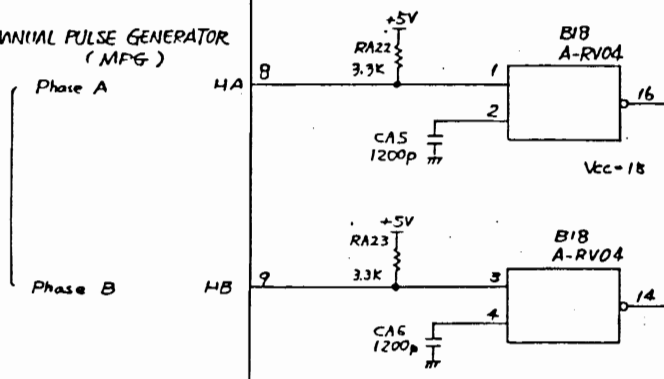
A H I	
16C-1000-0030/0031	

AWI-324321

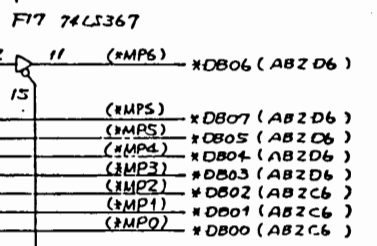
CAX5 (C07)



FROM MANUAL PULSE GENERATOR (MPG)

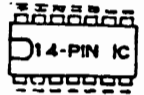


This buffer gate extends the sign bit to bit 6 in read operation.



(AA1F9) *PCL3
 (AB5B10) *RRE7E4
 (AA1D9) *CLK1

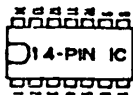
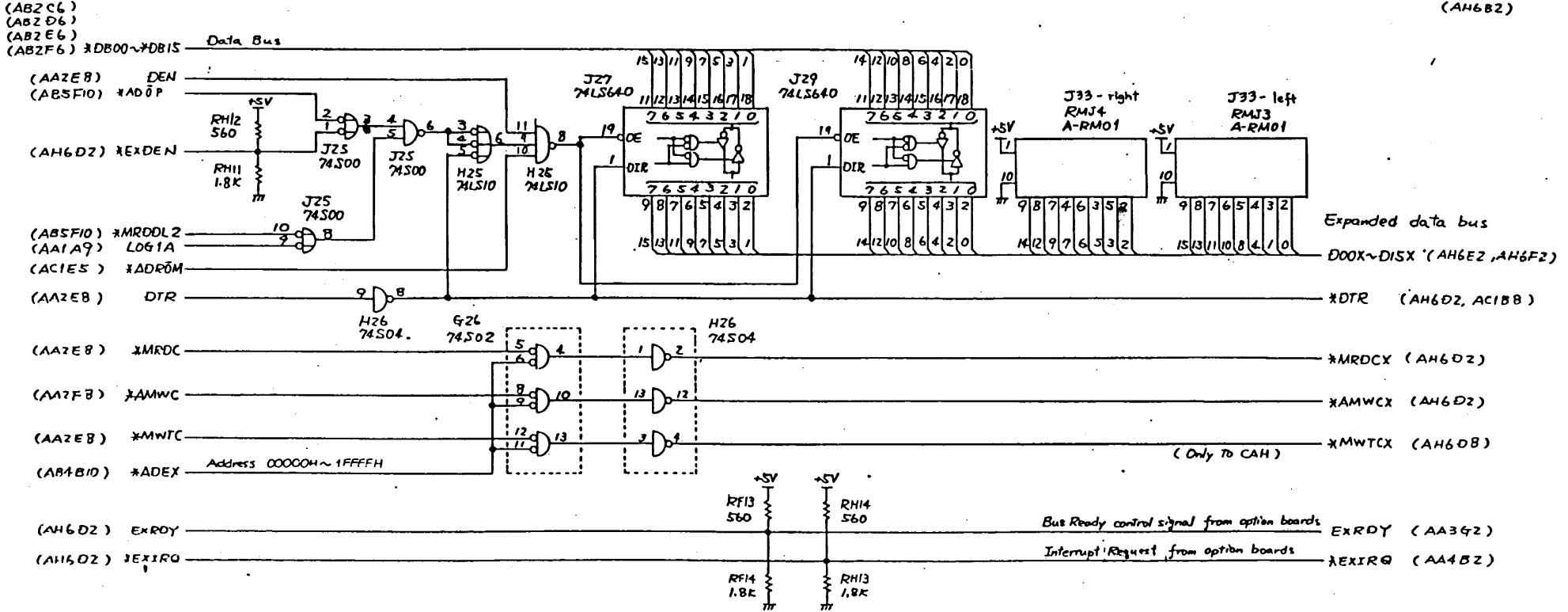
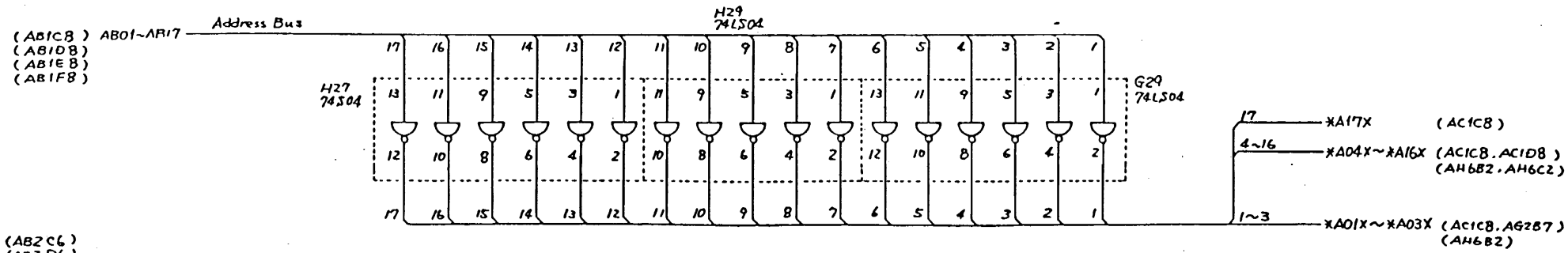
(DI) 0E7E4 (*MP6) (*MP5) (*MP5) (*MP4) (*MP3) (*MP2) (*MP1) (*MP0)



MANUAL PULSE GENERATOR INTERFACE

A M 3									
116C-1000-0030									
0031									

AWL-324323



EXPANDED BUS INTERFACE

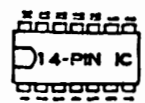
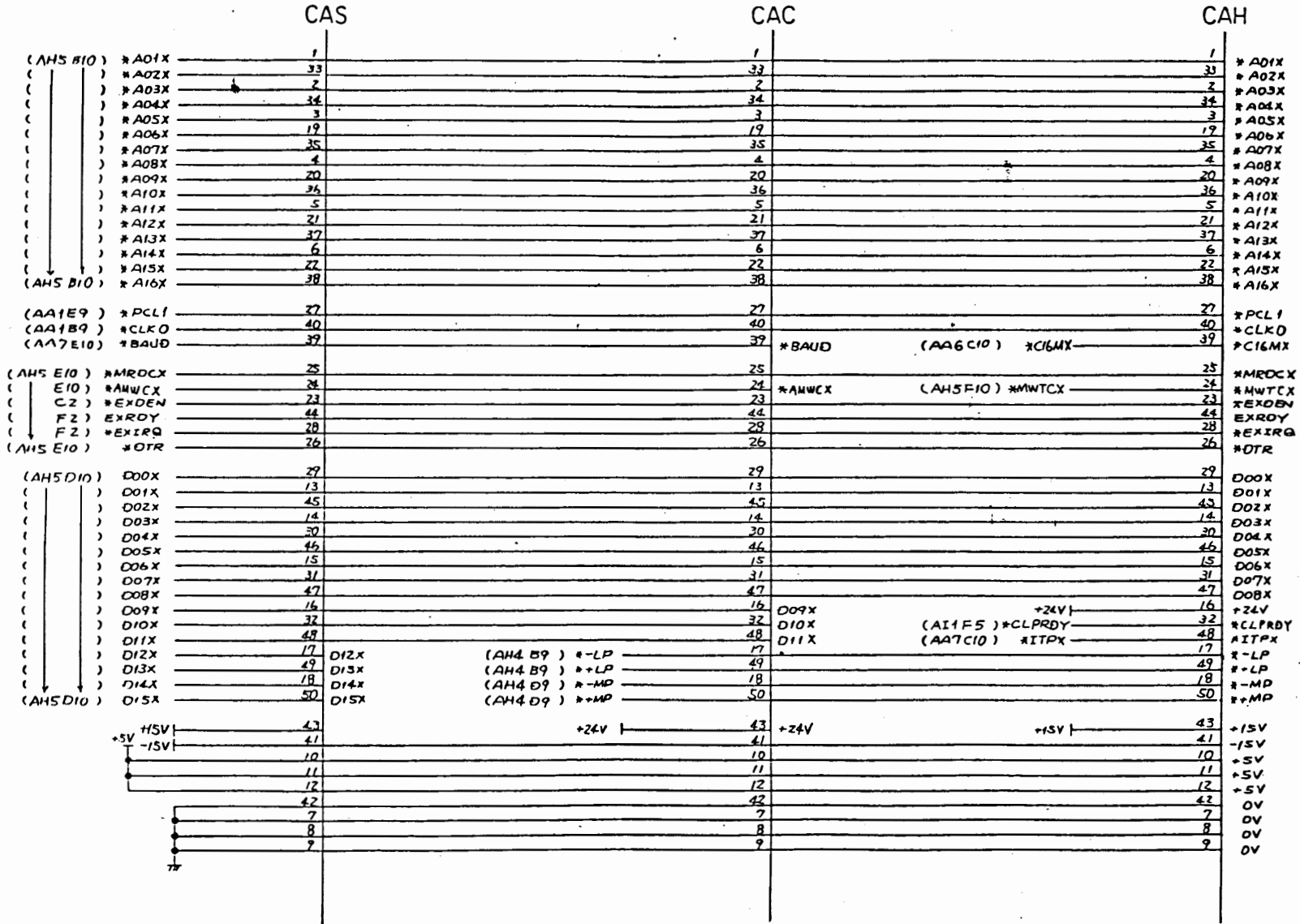
AHS	
1	2
3	4
5	6
7	8
9	10
11	12
13	14
15	16
17	18
19	20
21	22
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73	74
75	76
77	78
79	80
81	82
83	84
85	86
87	88
89	90
91	92
93	94
95	96
97	98
99	100

A16C-1000-0030
0031

AWI-324324

TO/FROM PC model B BOARD

TO/FROM ADDITIONAL AXIS
& ANALOG SPINDLE BOARD



EXPANDED BUS INTERFACE

A H G	
A16C-1000-0030/0031/01	

53
#

AWI-324325

(AB2C6, AB2D6) *DB00~*DB07

(AA2E8) DTR

(AB4F10) *A0BM
(AB4F10) *ADPOX
(AB4F10) *ADPOSX
(AB4F10) *ADPOZY
(AB4G10) *ADPTC
(AA2E8) *INTA
(AB4G10) *ADPTT
(AA2E8) DEN

(AB1C8, AB1D8) AB01~AB07

(AA1A9) LOG1A

(AB5B10) *WE7C6

(AB2C6) *DB00

(AB2C6) *DB01

(AB2C6) *DB02

(AB2D6) *DB06

(AB2D6) *DB04

(AB2D6) *DB07

(AF3G10) *MSROYO

(AA6F10) *WDALM

(AB5C10) *WE7C7

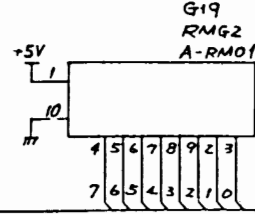
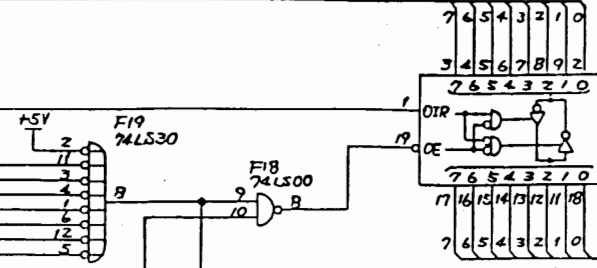
(***F5) PRDY02

(AB2E6) *DB08

(AB2E6) *DB09

(AB2E6) *DB10

(AA1F9) *PCL3

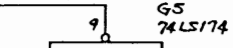
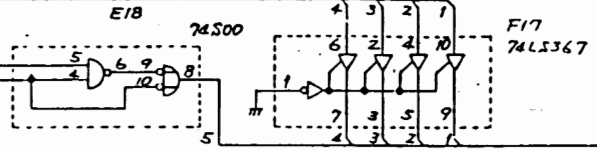


DATA BUS FOR MOS DEVICES
- POSITION CONTROL M8879E, AE
- PIC 8259A
- PIT 8253-5

DO0I~DO7I (AA4B10, AA7A2)
(AI2B1)
MOSDV (AA3E2)

A01I (AA4B2, AA7A2, AI2B1)
A02I (AA7A2, AI2B1)

A03I~A05I (AI2B1)



(AI3B10) FBAX

(AI3B10) FB0X

(AI3C10) PCX

(AI4C10) WBALX

(AI4B10) FBAY

(AI4B10) FBBY

(AI4C10) PCY

(AI4C10) WBALY

(AB5D10) *RE7C8

(AI5A10) FBAZ

(AI5B10) FBBZ

(AI5B10) PCZ

(AI5C10) WBALZ

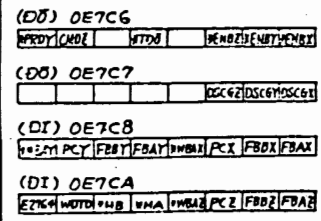
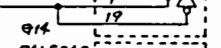
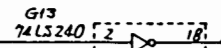
(AH3F10) *HA

(AH3F10) *HB

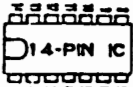
(AA6G10) WDT0

(AC1B5) E2164

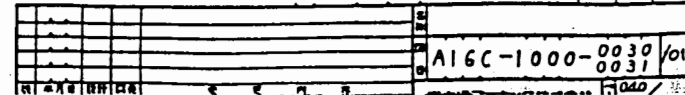
(AB5D10) *RE7CA



(D0) OE7C6
PRDY02, CMDZ, WDT0, DEN, DEN, DEN, DEN, DEN, DEN
(D0) OE7C7
DSCG2, DSCG5, DSCG6
(D1) OE7C8
PCY, PCY, FBBY, FBAY, WBALY, PCX, FB0X, FBAX
(D1) OE7CA
E2164, WDT0, *HB, *HA, WBALZ, PCZ, FBBZ, FBAR



POSITION CONTROL



AWI-324325
116C-1000-0030/01
0031
1040

AWI-324328

(A65F10) *MRDDLZ
(A6A2E8) *MMWTC
(A6A7C10) *ATTP
(A61C10) A011~A05T
(A61D10)

(A61C10) D001~D07I

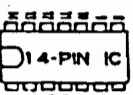
(A66C10) *C16M1

(A64F10) *ADPO5X

(A16E10) VREFS Reference 5V
(A16F10) VREFC " 2.5V

(A13B10) FBAX Feedback Pulse A
(A13B10) FBAX Feedback Pulse B
(A13C10) PCX One Revolution

(A61F8) *PCL3 Reset



Note. Position control LSI should be MB8737E or MB8739AE.
Neither MB8737N nor MB8739AN can be used.

POSITION CONTROL LSI

Y/Z AXIS

(A66C10) *C16M0

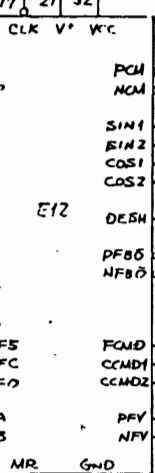
(A64F10) *ADPO5Y

(A16E10) VREFS
(A16F10) VREFC

(A14B10) FBAY
(A14B10) FBBY
(A14C10) PCY

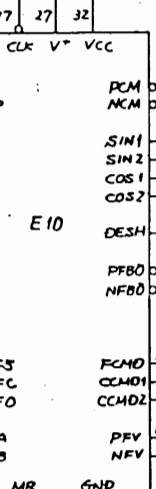
(A61F8) *PCL3

+15V +5V
17 27 32



X AXIS

+5V +5V
17 27 32



Z/P AXIS

(A66C10) *C16M0

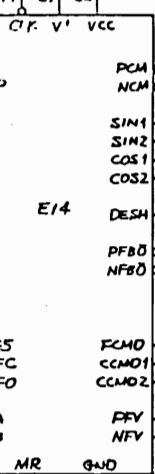
(A64F10) *ADPO5Z

(A16E10) VREFS
(A16F10) VREFC

(A15A10) FBAZ
(A15B10) FBBZ
(A15B10) PCZ

(A61F8) *PCL3

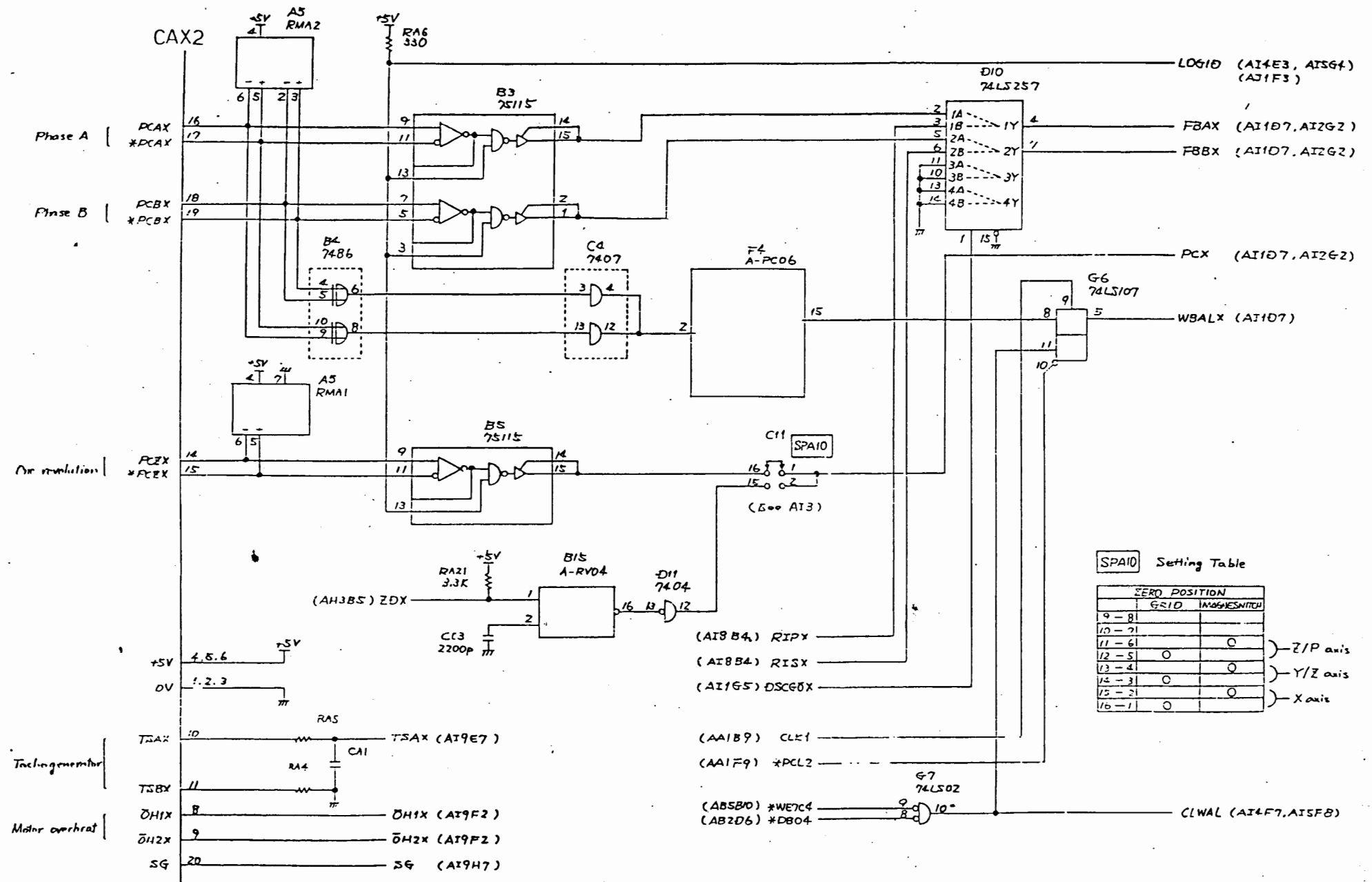
+15V +5V
17 27 32



A I 2

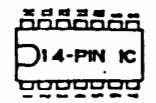
Table with columns for part numbers and quantities. Includes entries like A16C-1000-0030/01 and A16C-1000-0031/01.

AWI-32432T



SPA10 Setting Table

ZERO POSITION			
GRID	IMAGESWITCH		
9-8			
10-7			
11-6	0	0	Z/P axis
12-5	0	0	
13-4		0	Y/Z axis
14-3	0	0	
15-2		0	X axis
16-1	0	0	

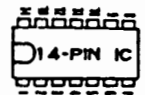
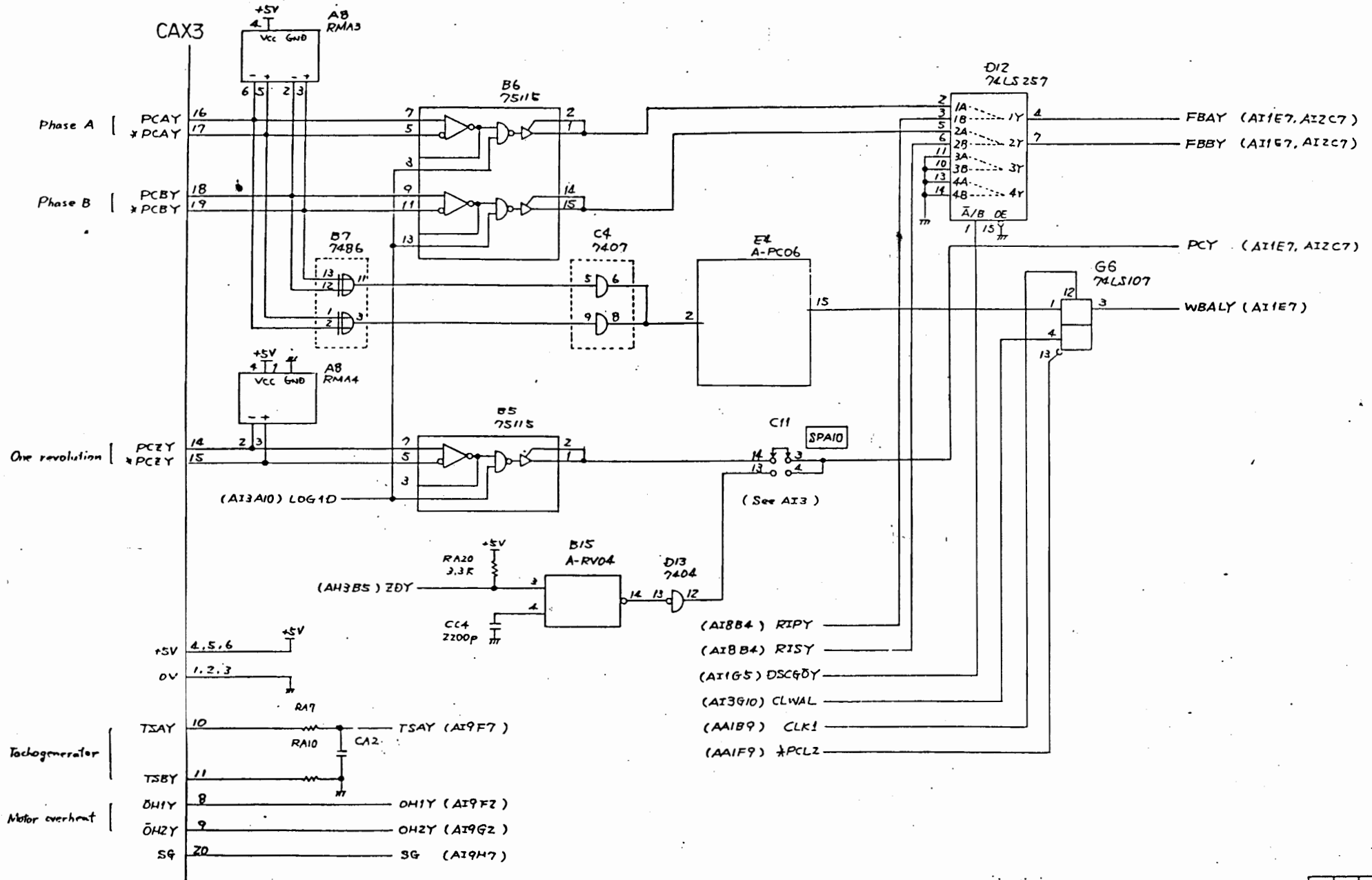


X-AXIS POSITION FEEDBACK

A I 3

AWI	32432T	REV	1	DATE	10/42
A16C-1000-0030/01					

AWI-32432B



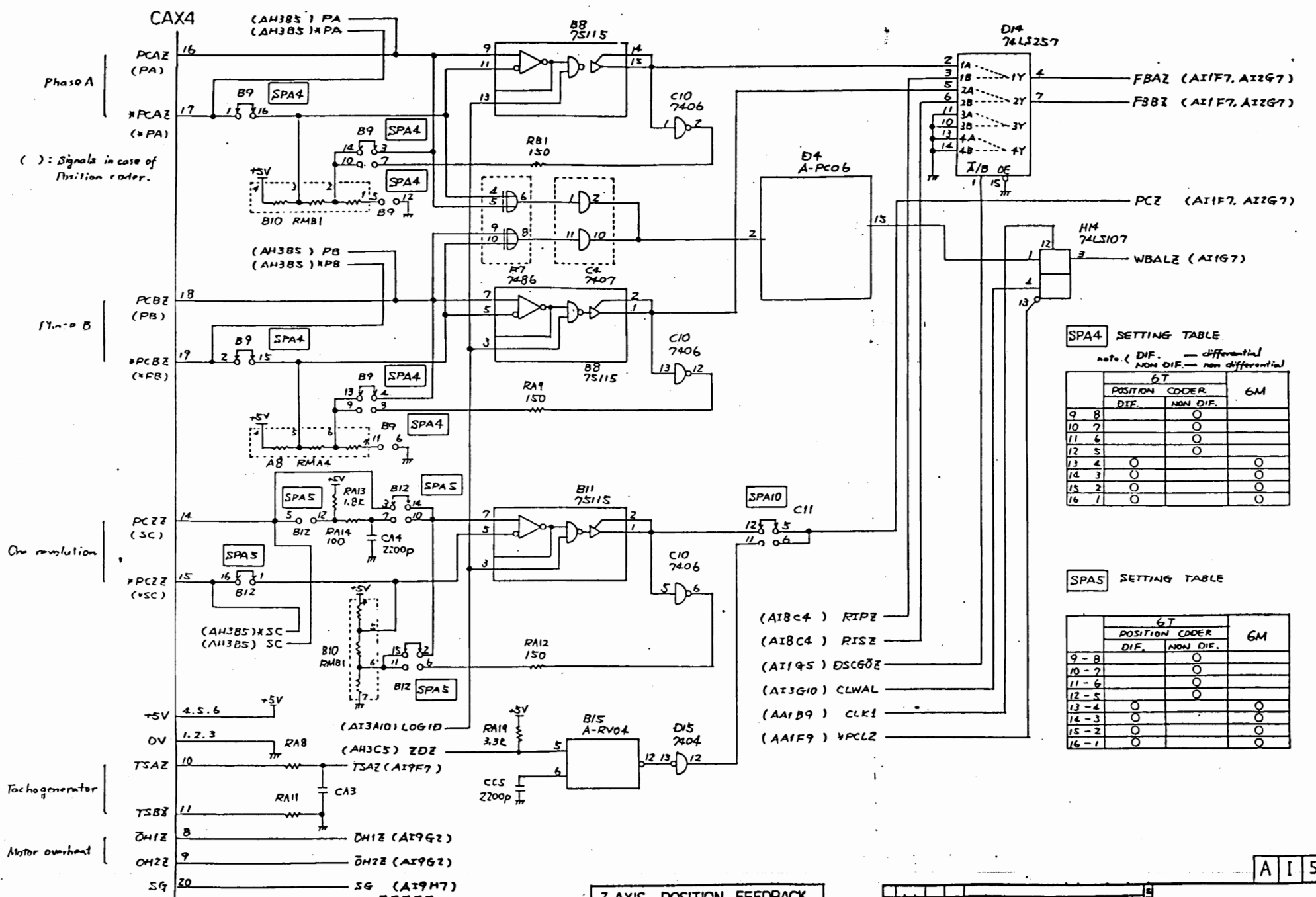
Y/Z AXIS POSITION FEEDBACK

A14

REV	1	DATE	10/01
REV	2	DATE	10/01
REV	3	DATE	10/01
REV	4	DATE	10/01
REV	5	DATE	10/01
REV	6	DATE	10/01
REV	7	DATE	10/01
REV	8	DATE	10/01
REV	9	DATE	10/01
REV	10	DATE	10/01
REV	11	DATE	10/01
REV	12	DATE	10/01
REV	13	DATE	10/01
REV	14	DATE	10/01
REV	15	DATE	10/01
REV	16	DATE	10/01
REV	17	DATE	10/01
REV	18	DATE	10/01
REV	19	DATE	10/01
REV	20	DATE	10/01

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AWI-324329



(): Signals in case of Position coder.

Phase B

One revolution

Tachogenerator

Motor overheat

SPA4 SETTING TABLE
 note: (DIF. — differential
 NON DIF. — non differential

		6T POSITION CODER		GM
		DIF.	NON DIF.	
9	8			
10	7			
11	6			
12	5			
13	4	○		○
14	3	○		○
15	2	○		○
16	1	○		○

SPAS SETTING TABLE

		6T POSITION CODER		GM
		DIF.	NON DIF.	
9-8				
10-7				
11-6				
12-5				
13-4	○			○
14-3	○			○
15-2	○			○
16-1	○			○

- (A18C4) RIPZ
- (A18C4) RISZ
- (A19F5) DSCG0Z
- (A13G10) CLWAL
- (AA1B9) CLK1
- (AA1F9) VPCL2

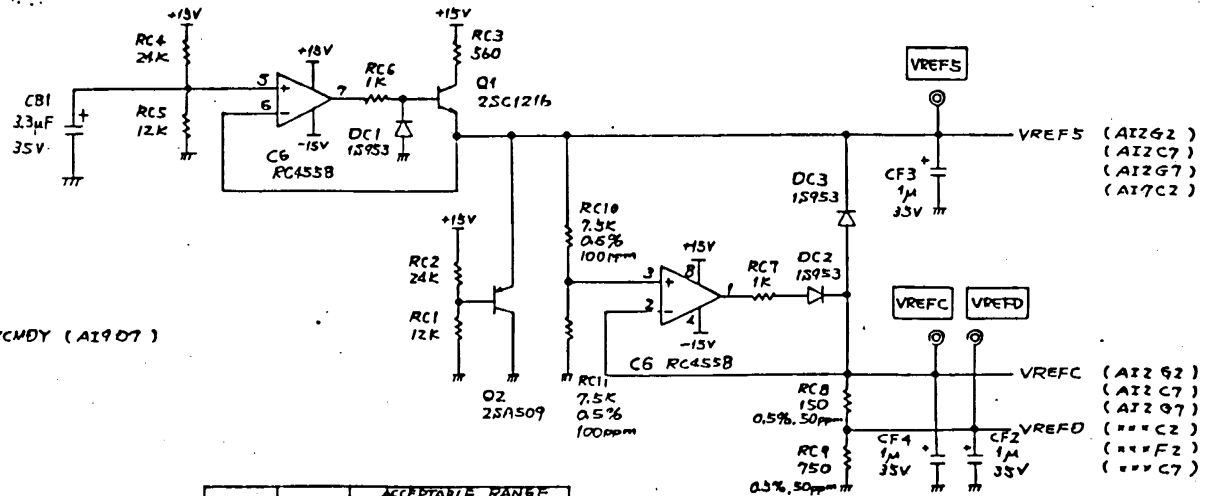
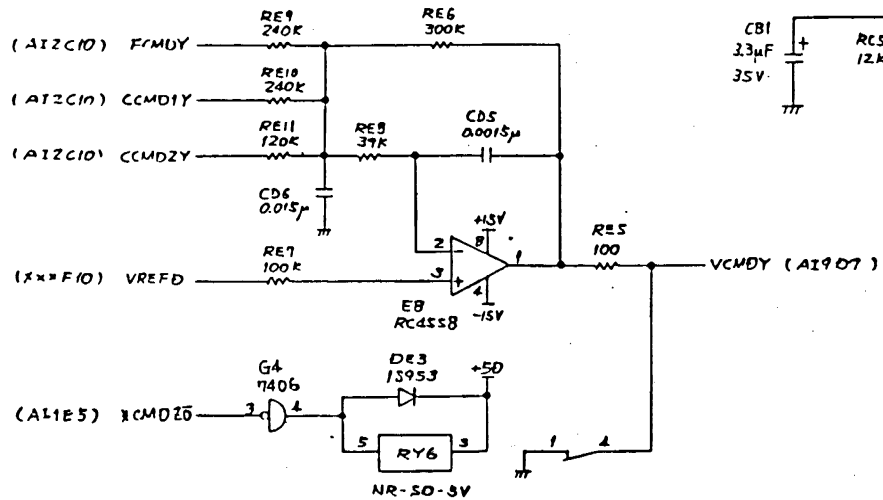
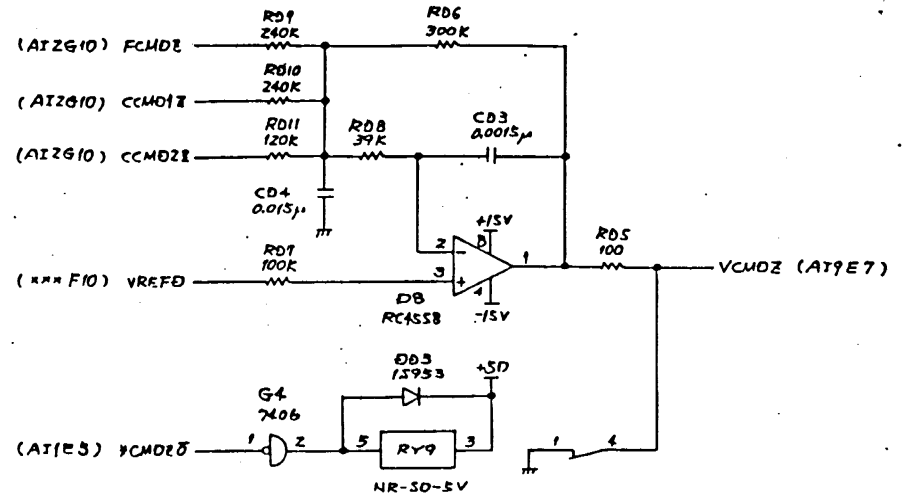
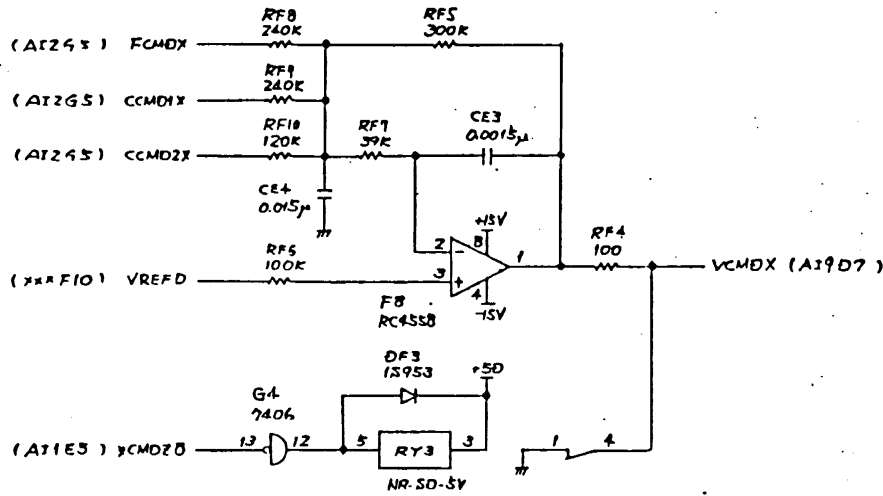
Z AXIS POSITION FEEDBACK
 POSITION CODER INTERFACE

14-PIN IC

A I S

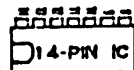
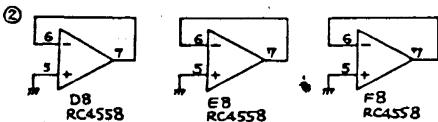
A16C-1000-0030/01
 0031/01

AWI-324330



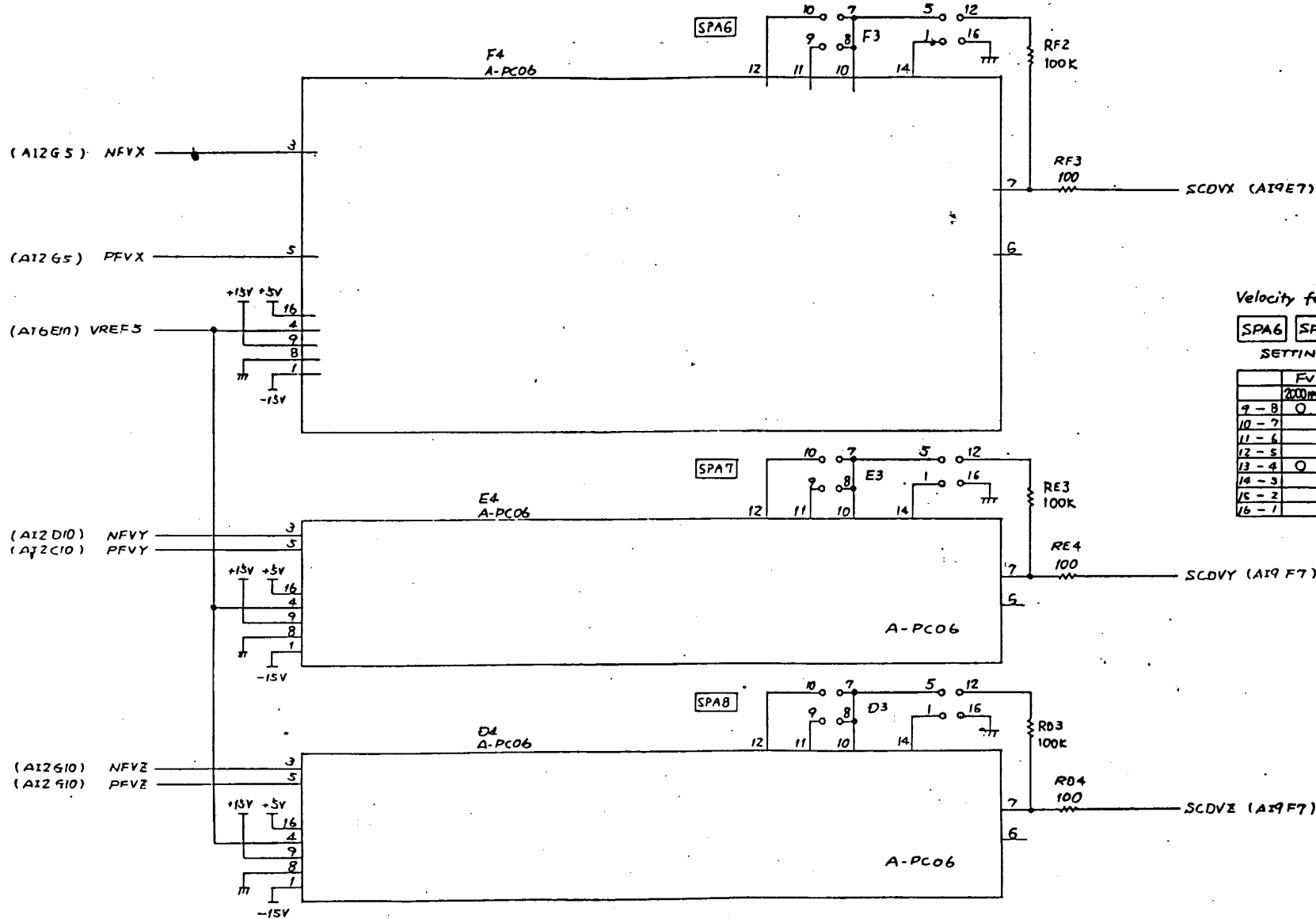
	NOMINAL	ACCEPTABLE RANGE	
		MIN	MAX
VREF5	5.0V	4.85V	5.15V
VREFC	2.5V	0.495VREF5	0.505VREF5
VREFD	2.08V	0.825VREFC	0.842VREFC

Note: Accuracy rank of resistors is 1% unless otherwise noted.



REFERENCE VOLTAGE CIRCUIT AND D/A CONVERTER

AWI-324331



Velocity feedback selection

SPA6 SPA7 SPA8

SETTING TABLE

	FV Converter			Tacho generator
	200 rev	250 rev	300 rev	
9-8	<input type="checkbox"/>			
10-7				
11-6		<input type="checkbox"/>		<input type="checkbox"/>
12-5			<input type="checkbox"/>	
13-4	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
14-3				<input type="checkbox"/>
15-2				
16-1				

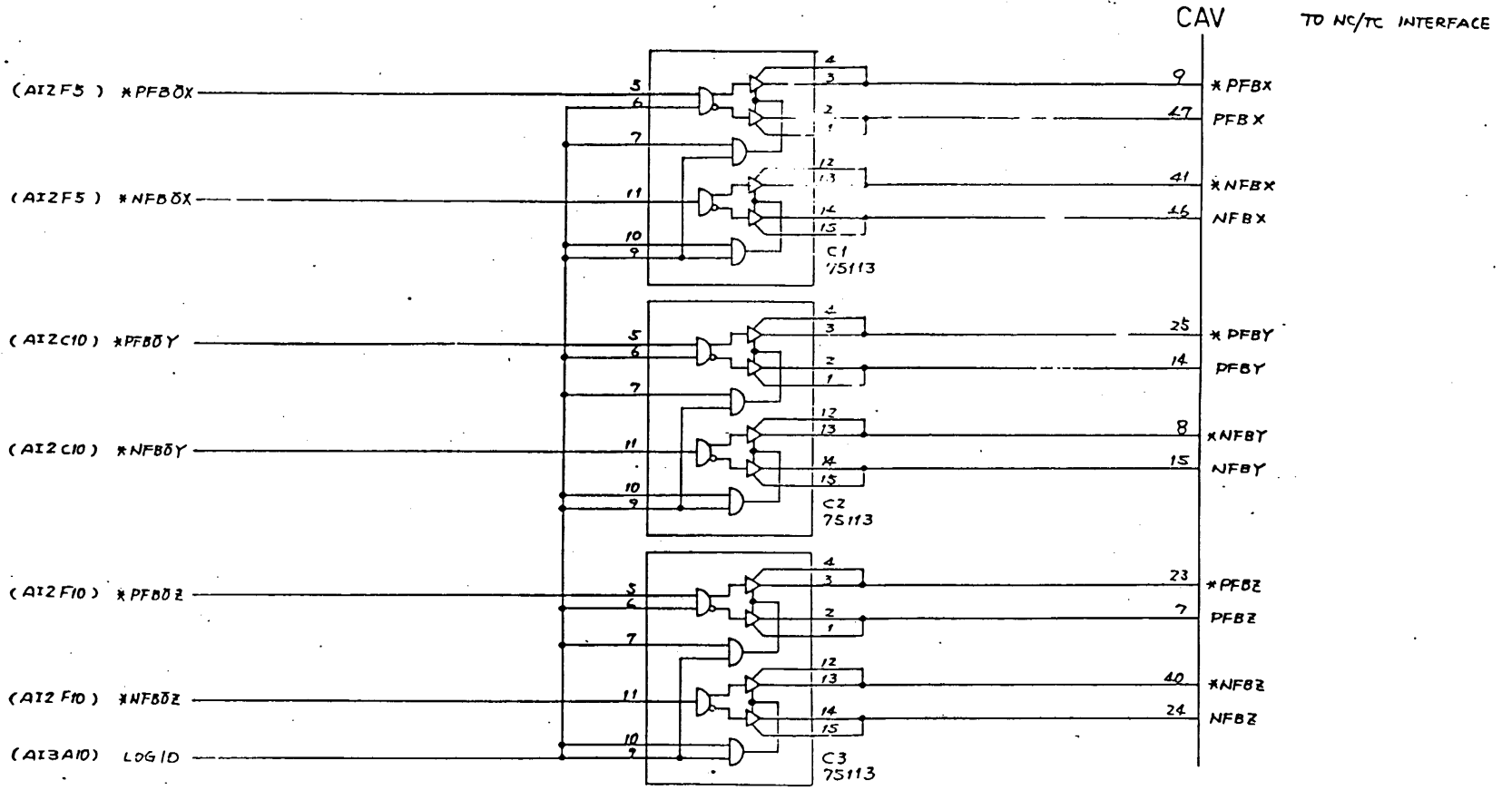
70
88

14-PIN IC

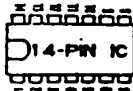
F/V CONVERTER

AI7
A16C-1000-0030/0031/01

AVI-324334



73



FEEDBACK PULSE OUTPUT

A J I		
A16C-1000-0030		
0031		
1099		

A VI-324335

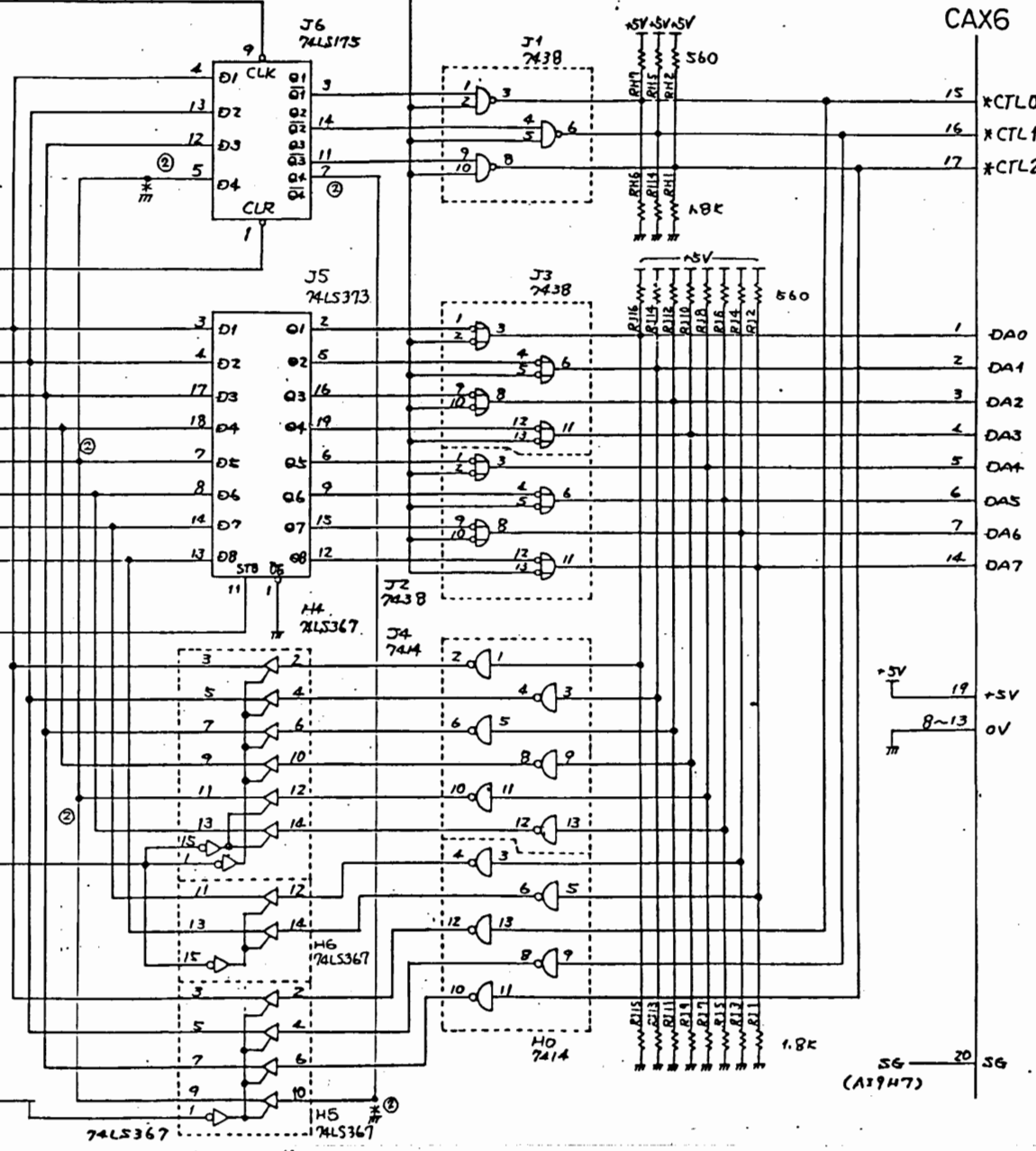
(AA1A9) L041A
(AB5E10) XWETCE

(AA1F9) *PCL2
(AB2C6) *DB00
(AB2C6) *DB01
(AB2C6) *DB02
(AB2D6) *DB03
(AB2D6) *DB04
(AB2D6) *DB05
(AB2D6) *DB06
(AB2D6) *DB07

(AB5D10) *WE7CC
(AB5D10) *RE7CC

(DI/DO) OE7CC	DA7	DA6	DA5	DA4	DA3	DA2	DA1	DA0
(DO) OE7CE					CTL2	CTL1	CTL0	
(DI) OE7CE			CHK		*CTL2	*CTL1	*CTL0	

(AB5D10) *RE7CE



CAX6

*CTL0
*CTL1
*CTL2
Control signals

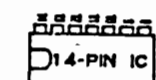
DA0
DA1
DA2
DA3
DA4
DA5
DA6
DA7
Data I/O

NOTE: In case that this interface is used for ;
 NC/TC INTERFACE (A20B-000B-0470) ,
 M-M INTERFACE (A20B-000B-0500) or
 MEAS. COUNTER (A20B-000B-0510)
 *CTL0~2 are defined as follows.

*CTL0 = ALP (Address latch pulse)
 *CTL1 = RW (Read/Write)
 *CTL2 = STRB (Strobe)

+5V
8~13
0V

Shield Ground
(A1947)



GENERAL PURPOSE INTERFACE

REV. 6/7/74	Modified for PCB version C23	AJ2	AIGC-1000-0030/0031/01
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